2024 IEEE 17th International Conference on Solid-State & Integrated Circuit Technology (ICSICT)

2024

https://www.icsict.com

FINAL PROGRAM

Oct. 22nd. - Oct. 25th. , 2024

Sheraton Zhuhai Hotel, Zhuhai, China

IEEE Beijing Section

FUDAN UNIVERSITY 復旦大學

SUN YAT-SEN UNIVERSITY 中山大學

PEKING UNIVERSITY 北京大學

ZHANGJIANG LABORATORY 张江实验室

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2024 IEEE 17th International Conference on Solid-State & Integrated Circuit Technology (ICSICT)

ICSICT 2024

Oct. 22nd - Oct. 25th , 2024
Sheraton Zhuhai Hotel, Zhuhai, Chna

Sponsored by

IEEE Beijing Section
Fudan University
Sun Yat-Sen University
Peking University
Zhangjiang Laboratory
National IC Innovation Center
Zhuhai Association for Science and Technology

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IEEE CASS
IEEE EDS Shanghai Chapter
IEEE SSCS Shanghai Chapter
IET Shanghai Network

Organized by

Fudan University
Sun Yat-Sen University
Zhangjiang Laboratory
National IC Innovation Center

Contents

Welcome to ICSICT 2024

On behalf of the Conference, we are delighted and honored to extend a warm welcome to all attendees of ICSICT 2024.

ICSICT 2024 will take place in Zhuhai, China, from October 22nd. to 25th., 2024. This marks the 17th. edition of our conference series, which began in 1986. After two years of virtual meetings due to COVID-19, we are thrilled to return to an in-person format.

The conference is an international forum for VLSI circuit designers, system integrators, IC manufacturers, device engineers, and CAD/CAE tool developers to showcase the latest advancements, developments, and research findings in their respective fields. It also provides an excellent platform for academic and industry participants to share information and interests.

ICSICT 2024 has invited international experts to conduct tutorials on the first day of the conference. Additionally, eight world-renowned academic and industry leaders will deliver keynote speeches during the plenary sessions.

ICSICT has significantly impacted on both industry and academia, and we look forward to continuing this tradition with you at this year's in-person conference.

General Co-Chairs of ICSICT 2024

Jan Van der Spiegel
David Wei Zhang
Shaozhi Deng
Bin Zhao
Francois Rivet

Oct. 23rd., 2024

Conference Committee

Life Honorary Chair

Name	Affiliation	Country/Area
Yangyuan Wang	Peking University	China

General Co-Chairs

Name	Affiliation	Country/Area
Jan Van der Spiegel	University of Pennsylvania	USA
David Wei Zhang	Fudan University, NICIC	China
Shaozhi Deng	Sun Yat-Sen University	China
Bin Zhao	IEEE EDS	USA
Francois Rivet	University of Bordeaux	France
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Mengqi Zhou	IEEE Beijing Section	China

Secretary-General

Xiaona Zhu	Fudan University	China

General Information

Conference Language

The official language is English. No simultaneous translation is available.

Conference Schedule

Date	Time	Event
Oct. 22nd. Tue.	AM	Tutorial Session (T1)
	PM	Tutorial Session (T2)
Oct. 23rd. Wed.	AM	Opening
		Keynote Session (K1, K2)
	PM	Parallel Sessions
		Parallel Sessions
		Poster Session (P1)
	Evening	Reception
Oct. 24th. Thur.	AM	Keynote Session (K3)
		Panel Discussion
	PM	Parallel Sessions
		Parallel Sessions
		Poster Session (P2)
Oct. 25th. Fri.	AM	Keynote Session (K4)
		Parallel Sessions
	PM	Parallel Sessions
		Parallel Sessions
	Evening	Closing & Banquet

Conference Site

The conference will be held in Sheraton Zhuhai Hotel, Zhuhai, Guangdong China

Tel: (86) (756) 2996688

Add: 1663 Yinwan Road, Wanzai Xiangzhou District, Zhuhai, Guangdong China

Registration Desk

The conference registration desk will be located at Sheraton Zhuhai Hotel. The conference registration will be open on Oct. 22nd. (8: 00~20: 00), Oct. 23rd. ~ Oct. 25th. (8: 00~17: 45). And the registration desk will keep available at the same site throughout the whole conference.

Transportation

How to get to Zhuhai:

You can take a flight/train to Zhuhai directly, or choose Hong Kong, Macao or Guangzhou as your destination and transfer to Zhuhai.

More details about the conference hotel booking, please visit

https://www.discoverchinatours.com/zhuhai-icsict-2024-hotel.html

Weather

The average temperature during conference time in Zhuhai is around 21℃~28℃.

Visa

All the foreign travelers to China must have a valid visa. Visas may be obtained from the Chinese Embassy or Consulate in most major cities around the world. A conference attendee will be mailed an official invitation letter for visa application after he or she fills and returns the Visa Application Form (https://www.icsict.com) to icsict_org@fudan.edu.cn timely.

Awards

Excellent Student Paper Awards & Outstanding Young Scholar Paper Award will be announced at the banquet on Oct. 25th. To be qualified for these Best Paper Award, the paper must be presented by the student or scholar him- or herself (first author). The Technical Program Committee and Organizing Committee will choose best papers through public appraisal from the candidates.

Paper Presentation Information

The ICSICT 2024 will have oral and poster sessions. All papers included in the conference program should be presented in English by one of the authors at the arranged sessions.

Oral Presentation

Presentation time:

Invited paper (25~30 minutes): 20~25 min talk + 5 min Q/A

Regular paper (12~15 minutes): 10~12 min talk + 2~3 min Q/A

Computer and digital projector will be provided in each meeting room.

Poster Presentation

Poster size: 120 cm (high) × 100 cm (wide)

Poster Session 1:

Setup time: 8: 30-17: 00 on Oct. 23rd.

Presentation time: 17: 15-18: 30 on Oct. 23rd. (on the spot)

Poster Session 2:

Setup time: 8: 30-17: 00 on Oct. 24th.

Presentation time: 17: 15-18: 30 on Oct. 24th. (on the spot)

Thumb pins, adhesive tapes, and scissors will be provided at the registration desk. The poster should be taken off by 21: 30 by the author if he or she would like to keep it. After that time, it will be removed and be regarded as being discarded by the authors.

Note

If authors want to update their papers, please contact volunteer at the venue， or email to icsict_org@fudan.edu.cn before closing of the conference.

Coffee Break

Complementary coffee/tea will be served in each morning/afternoon session. The break will take place in general at 10: 30-10: 45 during morning sessions and 15: 15-15: 30 during afternoon sessions. Due to time schedule of different sessions, the actual break time may have slight variation. Coffee/tea will be served in about half-hour duration.

Tutorial Session

Tuesday

Tuesday, October 22, 9: 00 – 18: 00

Tuesday, October 22, 9: 00 – 12: 15 Meeting Room 8
Tutorial Session T1 Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Dr. Albert Li, Lingyange Semiconductor Inc., China
Dr. Y.K. Li, Zhuhai Fudan Innovation Institute, China

T1-1	**Low-frequency Noise Characterization as a Diagnostic Tool to Characterize Advanced Semiconductor Materials and Devices**
9: 00 ~10: 30	Prof. Cor Claeys, Proximus, Belgium
	Coffee Break
T1-2	**Power Super-junction Devices**
10: 45 ~12: 15	Prof. Wentong Zhang, University of Electronic Science and Technology of China, China

Tuesday, October 22, 13: 30 – 18: 30 Meeting Room 8
Tutorial Session T2 Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Dr. Yuan Li, AzurEngine Technologies Inc., China

T2-1	**In-Memory Neuromorphic Computing Algorithm and Hardware**
13:30 ~15:00	Prof. Yufei Ma, Peking University, China
	Coffee Break
T2-2	**Artificial Intelligence for 6G: Implementations, Algorithms, and Optimizations**
15: 15 ~16: 45	Prof. Chuan Zhang, Southeast University, China
	Coffee Break
T2-3	**RF/mm-IC in silicon for wireless communication**
17: 00 ~18: 30	Prof. Hao Gao, Eindhoven University of Technology, The Netherlands

Technical Session

Wednesday

Wednesday, October 23, 8: 30 –9: 00

Wednesday, October 23, 8: 30 –9: 00 Grand Ball Room
Opening Sheraton Zhuhai Hotel 1st Floor

Wednesday, October 23, 9: 00 –10: 30

Wednesday, October 23, 9: 00 –10: 30 Grand Ball Room
Keynote Session K1 Sheraton Zhuhai Hotel 1st Floor
Session Chair: Prof. Bin Zhao, IEEE EDS, USA

K1-1	**Low-Power On-Device Computation for Future AI Expansion**
9: 00 ~ 9: 45	Dr. Paul Penzes, Vice President, Qualcomm, USA
K1-2	**Effective Deep Learning Models using Medical Images for Disease Diagnosis**
9: 45 ~10: 30	Prof. Myung Hoon Sunwoo, Ajou University, Korea
	Coffee Break

Wednesday, October 23, 10: 45– 12: 15

Wednesday, October 23, 10: 45–12: 15 Grand Ball Room
Keynote Session K2 Sheraton Zhuhai Hotel 1st Floor
Session Chair: Prof. Cor Claeys, Proximus, Belgium

K2-1	**Piezotronics of the third- and fourth-generation semiconductors**
10: 45 ~11: 30	Prof. Zhong Lin Wang, Georgia Institute of Technology, USA
K2-2	**Atomic Layer Processing: Its Evolution, Diverse Applications, and Future Prospects**
11: 30 ~12: 15	Prof. Fred Roozeboom, University of Twente, The Netherlands

Wednesday, October 23, 13: 30 – 17: 15

Wednesday, October 23, 13: 30 – 17: 15 Meeting Room 1
Special Session: the Future of AI Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Jianguo Yang, Zhangjiang Laboratory, China

	Title
SS-1	**Transforming AI: The Impact of Computing-in-Memory on Future Technologies**
13:30 ~14:00	Tony Tae-Hyoung Kim *(Nanyang Technological University, Singapore)*
SS-2	**Ultra-low power multi-core hardware accelerators for AI on Edge**
14:00 ~14:30	Do Anh Tuan *(A*STAR, Singapore)*
SS-3	**Large language Model on Chip**
14:30 ~15:00	Hao Yu *(Southern University of Science and Technology, China)*
SS-4	**Progress and Challenges of Multi-physics Simulation EDA for Chiplet Packaging**
15:00 ~15:30	Wenliang Dai *(Xpeedic Corp., China)*
	Coffee Break
SS-5	**Emerging Non-volatile and Non-volatile/Volatile Fused Computing-in-memory Macros for Edge Inference and Learning**
15:45 ~16:15	Chunmeng Dou *(University of Chinese Academy of Sciences, China)*
SS-6	**0459: Memristor Crossbar's Design Technology for Improving PPA (Power-Performance-Area) of Neural Networks**
16:15 ~16:45	Kyeong-Sik Min *(Kookmin University, Korea)*
SS-7	**0462: Reliability of Memristor-based Neuromorphic Computing System**
16:45 ~17:15	Michiko Inoue *(Nara Institute of Science and Technology, Japan)*

Wednesday, October 23, 13: 30 – 15: 15	Meeting Room 2
Session B1: Analog Circuit I	Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Wenning Jiang, Fudan University, China	

	Title
B1-1	**0014: Design and Verification of Low-Temperature-Drift and Capacitor-Less LDO Based on 110nm Technology (invited)**
13:30 ~14:00	Yonggang Fu, Anping He, Zhaobin Wang *(Lanzhou University, China)*
B1-2	**0056: A Self-adaptive Gamma Voltage Regulation Circuit for AMOLED Displays**
14:00 ~14:15	Zhifeng Mao, Fei Gou, Bin Sheng, Jing Xie, Wenwei Xu, Wei Liu, Jun Xu *(Glenfly Tech Co., Ltd., China; Tsinghua University, China)*
B1-3	**0131: A Reconfigurable Thermoelectric Energy Harvesting Interface Based on OPDC and DSCT**
14:15 ~14:30	Peiyuan Fu, Jiabin Wang, Xufeng Liao, Lianxi Liu *(Xidian University, China; Xidian University Chongqing ICs Innovation Institute, China)*
B1-4	**0325: A Fixed-Peak-Current Single-Inductor-MultipleOutput DC-DC Converter Achieving 92.6% Peak Efficiency**
14:30 ~14:45	Fei Liu, Langyuan Wang, Shuyu Zhang, Hanlu Zhang, Na Yan *(Fudan University, China; Common Mode (GONGMO) Semiconductor Co., Ltd., China)*
B1-5	**0211: Buck-Boost Converter with Stable Transition Mode for Low Average Inductor Current**
14:45 ~15:00	Ningning Li, Yibo Zhang, Yushen Zhang, Yizhe Yang, Wenhao Yang, Yimeng Zhang, Yuming Zhang *(Xidian University, China)*

Wednesday, October 23, 13: 30 – 15: 15	Meeting Room 3
Session C1: EDA I	Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Qi Wang, Zhuhai Fudan Innovation Institute, China	

	Title
C1-1	**0281: Deep Learning Design-Flow with Static and Dynamic Optimizations (invited)**
13:30 ~14:00	Zhiqiang Que, Jose G. F. Coutinho, Wayne Luk *(Imperial College London, UK)*
C1-2	**0476:A New Era for Ai Processor Design Methodology with High Level Synthesis (invited)**
14:00	Yuan Li *(XDL Technologies Inc., China)*

~14:30	
C1-3	**0270: A QEMU-Based Virtual Platform of MPSoC**
14:30 ~14:45	Liangquan Qiao, Lei Li, Xingyu Gao, Jinxiang Wang, Fangfa Fu, Keli Long, Jinghan Zhou *(Harbin Institute of Technology, China; 58th Research Institute of China Electronics Technology Group Corporation, China)*
C1-4	**0193: A Parallel Harmonic Balance Method Based on GPU for Efficient Periodic Steady-State Analysis**
14:45 ~15:00	Zhengzhuo Wang, Yanliang Sha, Lingyun Ouyang, Quan Chen, Jianguo Hu *(Sun Yat-sen University, China; Southern University of Science and Technology, China)*
C1-5	**0245: Efficient Dynamic Memory Management for High Bandwidth Memory on FPGA**
15:00 ~15:15	Yuwei Qu, Yiqing Mao, Wenbo Yin, Lingli Wang *(Fudan University, China)*

Wednesday, October 23, 13: 30 – 15: 15 Meeting Room 4
Session D1: Novel Device I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Zhengji Xu, Sun Yat-sen University, China

	Title
D1-1	**0312: Back End of Line (BEOL) Devices Using IGZO and P-type Oxides(Invited)**
13:30 ~14:00	John Robertson, Xuewei Zhang, Qingzhong Gui, Yuzheng Guo *(Cambridge University, UK; Wuhan University, China)*
D1-2	**0478:Miniaturization of High-speed GaN Based Laser Diodes(Invited)**
14:00 ~14:30	Junfei Wang, Chaowen Guan, Leihao Sun, Zhichong Wang, Chao Shen (Fudan University, China)
D1-3	**0122: Impact of Interfacial Layer on the Optoelectronic Performance of $MoTe_2$/Ge Heterojunction**
14:30 ~14:45	Wenyu Lei, Xiaokun Wen, Boyuan Di, Xinyue Xu, Haixin Chang, Wenfeng Zhang *(Huazhong University of Science and Technology, China)*
D1-4	**0098: MoS_2-WS_2 Heterostructure-enabled Optoelectronic Synaptic Diode**
14:45 ~15:00	Mingjie Li, Yingtao Ding, Jianzhi Hu, Hankun Zhao, Yilin Sun *(Beijing Institute of Technology, China）*
D1-5	**0472: Pseudo-Parallel Symmetrical and Crossed Perovskite Solar Cells for Bifacial Applications**
15:00	Guang-Wei Huang, Hsing-Mao Cheng, Jyi-Tsong Lin *(Sun Yat-Sen University,*

~15:15	*Taiwan, China)*

Wednesday, October 23, 13: 30 – 15: 15 Meeting Room 5
Session E1: Power Device I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Shaofeng Yu, Fudan University, China

	Title
E1-1	**0282: Gate Driver ICs for Wide Bandgap Power Transistors (invited)**
13:30 ~14:00	Wai Tung Ng, Rophina Li, Wentao Cui, Jingyuan Liang *(University of Toronto, Canada)*
E1-2	**0291: Suppression of Back-Gating Effect by Integrated Substrate Termination Network for 200V Monolithic GaN Half-Bridge Power IC**
14:00 ~14:15	Mengyao Zhao, Yifei Zheng, Yanfeng Ma, Yuan Sun, Denggui Wang, Chuanqi Pan, Jianjun Zhou, Sheng Li, Siyang Liu, Long Zhang, Weifeng Sun *(Southeast University, China)*
E1-3	**0326: High Short-Circuit Capability and Low-Loss SOI-LIGBT with Double-Integrated NMOS**
14:15 ~14:30	Jialei Tan, Jie Wei, Jinlong Lu, Xindi Liu, Gaoqiang Deng, Wei Song, Pei Guo, Bo Zhang, Xiaorong Luo *(University of Electronic Science and Technology of China, China; Chengdu University of Information Technology, China)*
E1-4	**0328: Body Diode Degradation Mechanism Of 1200V SIC Power MOSFETs Under Gamma Rays Total Ionizing Dose Irradiation**
14:30 ~14:45	Yu Tian, Zhaoxu Song, Hao Fu, Jiaxing Wei, Siyang Liu, Weifeng Sun *(Southeast University, China)*
E1-5	**0361: Novel Heterojunction Field Plate β -Ga2O3 MOSFET with High Breakdown Voltage**
14:45 ~15:00	Xiangnan Li, Jie Wei, Kai Zhao, Linyao Hao, Xiaosong Peng, Yuxi Wei, Renkuan Liu, Wei Song, Pei Guo, Xiaorong Luo *(University of Electronic Science and Technology of China, China; Chengdu University of Information Technology, China)*

Wednesday, October 23, 13: 30 – 15: 15 Meeting Room 6
Session F1: Memory Device I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Jian Huang, Sun Yat-sen University, China

	Title
F1-1	**0010: Sub-nanosecond Operation Speeds of Ferroelectric Domain Wall Memory (Invited)**
13:30 ~14:00	Anquan Jiang *(Fudan University, China)*
F1-2	**0096: Optimizing Flash Memory Towards Storage-Class Memory (SCM) Applications (Invited)**
14:00 ~14:30	Xinyi Guo, Yang Feng, Jing Liu, Junyu Zhang, Xuepeng Zhan, Jixuan Wu, Jiezhi Chen *(Shandong University, China; Institute of Microelectronics of Chinese Academy of Sciences, China; Neumem Co., Ltd, China)*
F1-3	**0020: Investigation of Reliability Characteristics of $Hf_xZr_{1-x}O_2$-Based FeFET and AFeFET Non-Volatile Memory**
14:30 ~14:45	Min Liao, Xianzhou Shao, Junshuai Chai, Xiaoqing Sun, Xiaoyu Ke, Hao Xu, Jinjuan Xiang, Xiaolei Wang, and Wenwu Wang *(Institute of Microelectronics, Chinese Academy of Sciences, China; Beijing Superstring Academy of Memory Technology, China)*
F1-4	**0066: Deep Understanding of Charge Trapping Phenomenon in n-FeFET and Endurance Improvement by Interlayer Engineering**
14:45 ~15:00	Saifei Dai, Hao Xu, Fengbin Tian, Xianzhou Shao, Xiaoqing Sun, Junshuai Chai, Xiaolei Wang, Wenwu Wang *(Institute of Microelectronics, Chinese Academy of Sciences, China; University of Chinese Academy of Sciences, China)*
F1-5	**0407: An FPGA-based Dual-mode SSD for Device-side Performance Optimization**
15:00 ~15:15	Xingyu Chen, Sirui Peng, Hankun Lv, Zhangbin Yang, Daixiao Peng, Xi Cai, Xueguang Lian, Yong Ding, Xiaoyong Xue *(Fudan University, China; University of Chinese Academy of Sciences, China; Institute of Electrical Engineering, Chinese Academy of Sciences, China; China Three Gorges Construction Engineering Corporation, China; Zhejiang University, China)*

Wednesday, October 23, 15: 30-17: 15

Wednesday, October 23, 15: 30-17: 15 Meeting Room 2
Session B2: Analog Circuit II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Wenning Jiang, Fudan University, China

	Title
B2-1	**0152: Dual-Loop Reference-less CDR with HLD for Wide Lock-in Range (invited)**

15:30 ~16:00	Chua-Chin Wang *(Sun Yat-Sen University, Taiwan, China)*
B2-2	**0089: A SET Sensitive Model of LC and Ring Voltage Controlled Oscillator in FinFET Technology**
16:00 ~16:15	Liu Heyuan, Yuan Hengzhou, Lu Jianzhuang, Chen Xiaowen, Sang Hao, Liu Jingtian, Guo Yang *(National University of Defense Technology, China; Academy of Military Sciences PLA China, China)*
B2-3	**0093: A Low Spur Wideband PLL in 65nm CMOS**
16:15 ~16:30	Zijun Wang, Biao Li, Teng Wang, Hong Li, Ruiting Niu, Jinpeng Lin *(Space Star Technology Limited Corporation, China)*
B2-4	**0217: A Low Power PLL Circuit with Signal 50% Duty Cycle Corrected in 180nm CMOS**
16:30 ~16:45	Bangtian Li, Xueke Li, Liying Chen, Chuantong Cheng, Jian Mei *(Tiangong University, China; Institute of Semiconductors, Chinese Academy of Sciences, China)*
B2-5	**0156: MTJ based Compensation for Charge Pump Temperature Drift**
16:45 ~17:00	Yongliang Zhou, Jingxue Zhong, Chengxing Dai, Yingxue Sun, Xin Li, Chunyu Peng *(Anhui University, China; Anhui Anxin Electronic Technology Co., Ltd, China）*
B2-6	**0213: A 112-Gb/s Coherent Receiver with a Novel Modulation Format**
17:00 ~17:15	Tianyuan Zhong, Boyang Zhang, Weixin Gai *(Peking University, China; Beijing Advanced Innovation Center for Integrated Circuits, China)*

Wednesday, October 23, 15: 30-17: 15 Meeting Room 3
Session C2: EDA II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Qi Wang, Zhuhai Fudan Innovation Institute, China

	Title
C2-1	**0292:An Improved Clock-Aware Global Placement Algorithm (invited)**
15:30 ~15:55	Ziang Ge, Pingqiang Zhou *(Shanghaitech University, China)*
C2-2	**0411: Analyzing Timing in Shorter Time: A Journey through Heterogeneous Parallelism for Static Timing Analysis (invited)**
15:55 ~16:20	Zizheng Guo, Yibo Lin, Runsheng Wang, Ru Huang *(Peking University, China)*
C2-3	**0216: TBPart-b: An Effective Hypergraph Partitioning Algorithm Considering Topological Order Balance for Processor-based Hardware Emulation**

16:20 ~16:34	Jing Tang, Shunyang Bi, Hailong You *(Xidian University, China)*
C2-4	**0068: FCE: A Fast CGRA Architecture Exploration Framework**
16:34 ~16:48	Sichao Chen, Yiqing Mao, Yuan Dai, Xuchen Gao, Wai-Shing Luk, Wenbo Yin, Lingli Wang *(Fudan University, China)*
C2-5	**0179: Research on Parametric Subthreshold Cell Delay Modeling Based on ANN**
16:48 ~17:02	Xuelian Zhang, Yuping Wu, Zhiqiang Li, Donglin Liu, Shushan Qiao *(Institute of Microelectronics of Chinese Academy of Sciences, China; University of Chinese Academy of Sciences, China)*
C2-6	**0393: A High-Performance Routing Architecture with 16 LUTs per CLB for Nanoscale FPGAs**
17:02 ~17:15	Sijing Yang, Jide Zhang, Hao Zhou, Lingli Wang *(Fudan University, China)*

Wednesday, October 23, 15: 30-17: 15 Meeting Room 4
Session D2: Novel Device II **S**heraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Zhengji Xu, Sun Yat-sen University, China

	Title
D2-1	**0450: Cryogenic and RF Modeling of On-Chip Passive Devices for Quantum Computer (Invited)**
15:30 ~16:00	Akira Tsuchiya *(The University of Shiga University, Japan)*
D2-2	**0457: Ferroelectric Transistors Based on Two Dimensional Materials (Invited)**
16:00 ~16:30	Wenwu Li *(Fudan university , China)*
D2-3	**0473: Comparison of Nanosheet and Fin Integration in Stacked Induced Tunnel Field-Effect Transistors**
16:30 ~16:45	Ruei-Cheng Tu, Chia-Yo Kuo, Jyi-Tsong Lin *(Sun Yat-Sen University, Taiwan, China)*
D2-4	**0160: Nonlinear Contact Behavior in MoS_2 Field Effect Transistors at Cryogenic Temperature**
16:45 ~17:00	Shihab Ahammed, Mansun Chan *(The Hong Kong University of Science and Technology, Hong Kong, China)*
D2-5	**0158: Experimental Verification of 1D Transport Model by Quantized Current**

	Spectrum of Si JNT Device
17:00 ~17:15	Zi-Meng Shang, Bo-Wei Wang, Wei-Hua Han *(Institute of Semiconductors,Chinese Academy of Sciences, China; University of Chinese Academy of Sciences, China)*

Wednesday, October 23, 15: 30-17: 15 Meeting Room 5
Session E2: Power Device II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Shaofeng Yu, Fudan University, China

	Title
E2-1	**0401: Investigation of SiON Passivation for High Performance AlGaN/GaN HEMTs (invited)**
15:30 ~16:00	Difei Fan, Chenkai Deng, Jiming Zhang, Peiran Wang, Nick Tao, Qing Wang, Hongyu Yu *(Southern University of Science and Technology, China; Maxscend Microelectronics Company Limited, China)*
E2-2	**0009: 180nm BCD Technology Platform with 8V to 65V Isolated LDMOS**
16:00 ~16:15	Qi Ding, Renxiong Li, Ning Ning, Jun Huang, Yutuo Guo, Yu Wang, Kunqin He, Yaxin Li, Huaishan Wang, Juan Tang, Qiuyue Huo, Minghong Yuan, Pan Peng, Ming Qiao, Lulu Peng, Bo Zhang *(United Microelectronics Center Co., Ltd, China; University of Electronic Science and Technology of China, China;)*
E2-3	**0200: A Novel Insulated Gate-Triggered Thyristor with Integrated Super-Clamp Gate Transient Voltage Suppressor for Ultrahigh di/dt Pulse Switching**
16:15 ~16:30	Shiyu Deng, Yuxiao Yang, Xinqi Sun, Pengwei Zhou, Ruize Sun, Chao Liu, Wanjun Chen, Bo Zhang *(University of Electronic Science and Technology of China, China)*
E2-4	**0210: Device Instability in the Third Quadrant of Schottky-Type p-GaN Gate HEMTs: The Hole Defficiency & Trapping Effect**
16:30 ~16:45	Kuangli Chen, Shuting Huang, Jinggui Zhou, Ning Yang, Jianggen Zhu, Enchuan Duan, Bo Zhang, Qi Zhou *(University of Electronic Science and Technology of China (UESTC), China)*
E2-5	**0236: Static Characteristic Recovery Of SiC MOSFETs Induced By Dynamic Gate Stress After Total Ionizing Dose Irradiation**
16:45 ~17:00	Jiahao Hu, Xiaochuan Deng, Xing Zeng, Tao Xu, Haibo Wu, Xuan Li, Bo Zhang *(University of Electronic Science and Technology of China, China)*

Wednesday, October 23, 15: 30-17: 15 Meeting Room 6
Session F2: Memory Device II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Jian Huang, Sun Yat-sen University, China

	Title
F2-1	**0141: A Simulation Comparison of Channel-All-Around and Gate-All-Around 3D Vertical Structure FeFET with IGZO Channel**
15:30 ~15:45	Xuebin Wang, Zhijian Guo, Yutao Li, Chengji Jin, Jixuan Wu, Guanhua Yang, Yuanxiao Ma, Masaharu Kobayashi, Fei Mo, Yeliang Wang *(Beijing Institute of Technology, China; The University of Tokyo, Japan; Institute of Microelectronics, Chinese Academy of Sciences, China; Shandong University, China; Xidian University, China)*
F2-2	**0174: Low Operating Voltage in HfO_2/ZrO_2 Superlattice Ferroelectric Capacitor Achieved by Thickness Scaling**
15:45 ~16:00	Dongya Li, Huan Liu, Peiyuan Du, Fei Yu, Chengji Jin, Xiao Yu, Yan Liu, Genquan Han, Yue Hao *(Xidian University, China; Zhejiang Lab, China; Hangzhou Institute of Technology, Xidian University, China)*
F2-3	**0257: Co-optimization of Oxide Semiconductor-based Ferroelectric Transistors Between Electrical Performance and Ambient Stability By Using TiO_2-IGZO Dual-Channel Layers**
16:00 ~16:15	Shangze Li, Xujin Song, Dijiang Sun, Xiaoyan Liu, Jinfeng Kang *(Peking University, China)*
F2-4	**0340: Enhancing Computational Precision in PLRAM-based In-memory Computing with High-Low Bit Concatenation**
16:15 ~16:30	Saike Zhu, Xiang Qiu, Yong Gong, Cimang Lu, Yi Zhao *(Zhejiang University, China; China Nanhu Academy of Electronics and Information Technology, China; East China Normal University, China; Flash Billion Semiconductor Co. Ltd., China)*
F2-5	**0062: FeFET based Logic-in-Memory Pipeline-Style Circuits**
16:30 ~16:45	Yang Li, Zhaohui Yang, Yinshui Xia *(Ningbo University, China)*
F2-6	**0172: Study of Vth Degradation Mechanism in FeFET with $TiN/Al_2O_3/HfO_2/Al_2O_3/Hf_{0.5}Zr_{0.5}O_2/SiO_x/Si$ Structure**
16:45 ~17:00	Runhao Han, Jia Yang, Tao Hu, Mingkai Bai, Yajing Ding, Xianzhou Shao, Saifei Dai, Xiaoqing Sun, Junshuai Chai, Hao Xu, Kai Han, Xiaolei Wang, Wenwu Wang, Tianchun Ye *(Institute of Microelectronics, Chinese Academy of Sciences, China; University of Chinese Academy of Sciences, China; Weifang University, China)*
F2-7	**0189: Random Number Generation from 3D-NAND Flash Memory Using Shallow Charge Trap Related Short-Term Retention Errors**
17:00 ~17:15	Ruibin Zhou, Jian Huang, Xianping Liu, Yuhan Wang, Xinrui Zhang, Yungen Peng, and Zhiyi Yu *(Sun Yat-sen University, China; Peng Cheng Laboratory, China)*

Wednesday, October 23, 17: 30 – 18：30

Wednesday, October 23, 17: 30 –18: 30
Poster Session I Sheraton Zhuhai Hotel 1st Floor

	Title
P1-1	**0007: A Fast-Response Current Source with High Impedance for Zero-Crossing-Based Circuits**
	Ruoyu Li, Xianglong Wang, Jianqiang Xu, Yintang Yang（*Xidian University, China*）
P1-2	**0028: Design of 12-bit Low-Power Single-Slope ADC with 2048 Columns for Infrared Focal Plane Array**
	Lixiang Han, Hao Li, Yihui Zhang, Liang Gao, Dongsheng Liu（*Huazhong University of Science and Technology, China; Hubei Optics Valley Laborator, China;*）
P1-3	**0040: A Low Power Narrow-Band Complex-Bandpass Filter Based on Feedforward Compensation Amplifiers for NB-IoT Applications**
	Xu Zhao, Ziqiang Wang, Jie Gan（*Beijing Smart-chip Microelectronics Technology Co.,Ltd, China; Tsinghua University ,China)*
P1-4	**0069: A 12V to 1V Tri-state DSD Hybrid Converter by Self-Balanced Dual Flying Capacitors with 0.3mV Output Ripple and 90.09% Peak Efficiency**
	Yixing Wang, Qianhui Liu, Yuhua Chen, Yizhe Yang, Yimeng Zhang, Yuming Zhang *(Xidian University, China;*）
P1-5	**0077: A Multi-Phase Clock Self-Calibrating Circuit**
	Zhihuai Li, Li Jiang, Xueming Wei, Zilu Cai, Jiami Tang *(Guilin University of Electronic , China)*
P1-6	**0090: A 18V, 600mA Load Current, 22MHz High-Voltage Power Amplifier with Over-Temperature Protection and Bidirection Enable Logic**
	Yuan Ren , Xin'an Wang *(Peking University, China)*
P1-7	**0153: A 10-MHz Four-Phase Hysteretic Control DC-DC Converter with Inductor Current Self-balancing**
	Yushen Zhang, Yibo Zhang, Yizhe Yang, Ningning Li, Wenhao Yang, Yimeng Zhang , Yuming Zhang *(Xidian University, China)*
P1-8	**0227: A High Precision -40 °C to 150 °C Bandgap Reference with Dual Temperature Compensation**
	Yuhan Zhang, Jianzheng Li, Xiaomeng An, Lina Wang, Yajie Qin（*Fudan University, China*）

P1-9	**0241: A Biphasic Neural Stimulator with Adaptive Pulse-Width Modulation Charge Balancer**
	Hailong Tang, Wenxian Gu, Yifan Song, Hengchang Bi, Xing Wu, Liangjian Lyu *(East China Normal University, China)*
P1-10	**0250: A PPG Analog Front-End With PVT-Insensitive High-Pass Frequency**
	Zhaofeng Huang, Zepeng Huang, Hengchang Bi, Xing Wu, Liangjian Lyu *(East China Normal University, China)*
P1-11	**0330: A Fully integrated FVF based low-noise voltage buffer for ADC reference**
	Ikhwan Kim, Yajie Qin *(Fudan University, China)*
P1-12	**0387: A Resistor-Free Grounded High-Frequency Memristor Emulator**
	Xinying Su, Bingjun Xiong, Junjie Yu and Jingjing Liu *(Sun Yat-Sen University, China)*
P1-13	**0027: An Ultra-Low-Leakage Current Sensing Interface for Wide Temperature Range**
	Jinsheng Tang, Chun Zhao, Lin He *(Nanjing University of Posts and Telecommunications, China)*
P1-14	**0029: A Global Threshold Voltage Finder Technology for the Readout Circuit of Event-based Vision Sensor**
	Yanwen Su, Hao Li, Dongsheng Liu, Ang Hu, Kaiyue Li *(Huazhong University of Science and Technology, China; Hubei Optics Valley Laboratory, China)*
P1-15	**0107: A Residue Amplifier With 72.27 dB Loop-Gain and 4.64 GHz Closed Loop Bandwidth consuming 6.4 mW for 12-Bit 1-Gsps Pipelined ADC**
	Jiangbo Wei, Jin Liu, Wei Tian, Chao Wang, Maliang Liu *(Xi'an Microelectronics Technology Institute, China; Xidian University, China)*
P1-16	**0124: A 180 mV–1.6 V Thermoelectric Energy Harvesting Converter with Low-Voltage Cold Start and Less than 1 μW Power Loss**
	Chunlin Wang, Anzhi Yan, Tianyu Guo, Peng Wan, Houfang Liu, Yi Yang, Tianling Ren *(Tsinghua University, China)*
P1-17	**0150: A Signal Conditioning ASIC With High Precision and Low Noise for MEMS Accelerometers**
	Quan Sun, Rui Liu, Zhe Zheng, Lei Dong, Ji-jiang Wang *(Xi 'an Aerosemi Technology. Co., Ltd., China; Beijing Smart-chip Microelectronics Technology Co., China)*
P1-18	**0185: Design of a high-precision self-calibration readout circuit for CMOS**

	microbolometer
	Qianhao Zhang, Jie Liu, Sheng Xu, Yiming Liao, Feng Yan, Xiaoli Ji *(Nanjing University, China; Nanjing University of Science and Technology, China)*
P1-19	**0297: A Smooth Two-Stage Soft Start Method for Current Mode Boost Converter**
	Yue Shi, Shi-dong Wang, Zekun Zhou, Bo Zhang, Zhigang Qin *(University of Electronic Science and Technology of China, China; Chengdu University of Information Technology, China; Saitama Institute of Technology, Japan)*
P1-20	**0327: A High-Voltage Smooth Self-Starting Reference Current Source Circuit**
	Dongyan Zhao, Jie Pa , Chenghao Zhang, Yidong Yuan, Yi Hu, Hongwei Shen，Zekun Zhou, Member, IEEE*(Beijing Smart-chip Microelectronics Technology Co., Ltd., China; University of Electronic Science and Technology of China, China)*
P1-21	**0276: A Temporal and Spatial Reuse Interpolation Hardware for VVC Motion Compensation**
	Huanxiang He, Shushi Chen, Leilei Huang, Yibo Fan *(Fudan University, China)*
P1-22	**0293: A Broadband Digital Beamforming Method Based on FPGA**
	Yiwen Tang, Guowen Jia, Zhen Zhang and Yue Zhang *(Sun Yat-Sen University, China)*
P1-23	**0305: Conditional cycle termination RANSAC**
	Tong Jiang, Yujie Huang, Liyuan Peng, Mingyu Wang, Wenhong Li,Minge Jing, Xiaoyang Zeng *(Fudan University, China; Shanghai ExploreX Technology Co., Ltd., China)*
P1-24	**0371: A multi-resolution propagation algorithm and pixel grouping storage strategy for PatchMatch Stereo**
	Kai Liu, Zhenyu Zhang, Haiwei Wang, Leilei Huang, Chunqi Shi, Long Xu, Runxi Zhang *(East China Normal University, China)*
P1-25	**0378: An XOR Arbiter PUF based on the IGZO TFT Devices**
	Xiang Chen, Yongliang Chen, Xiaole Cui *(Peking University Shenzhen Graduate School, China)*
P1-26	**0052: Design and Implementation of Hierarchical Storage Structure for MCCSIP-RAA**
	Longmei Nan, Yu Jin, Yiran Du, Tao Chen, Yanjiang Liu, Wei Li *(Institute of Information Science and Technology, China)*
P1-27	**0242: A Highly Scalable Hardware HEVC Encoder Based on FPGA**
	Guohao Xu, Chenlong He, Shiyan Yi, Leilei Huang, Xiaoyang Zeng, Yibo Fan *(Fudan University, China; East China Normal University, China)*

P1-28	**0259: A Hardware-friendly Fast Block Partition Decision Algorithm Based on Histogram of Oriented Gradient for AV1**
	Guohao Xu, Shiyan Yi, Zhijian Hao, Leilei Huang, Hao Zhang, Xiaoyang Zeng, Yibo Fan *(Fudan University, China; East China Normal University, China)*
P1-30	**0391: A High-Performance MTJ-LUT Circuit Using 4T1M Architecture**
	Yu Pan, Yuejun Zhang, Shuaicheng Guo,Yuanxin Tian, Bo Hong, Rui Fang, Liang Wen *(Ningbo University, China; China Coast Guard Academy, China)*
P1-31	**0261: Optimizing Communication Effciency of GNN Inference in Distributed System**
	Wenqian Zhou, Qiaosha Zou *(Fudan University, China; Zhejiang Lab, China)*
P1-32	**0295: SST: Simplified Space-Time Transformer based on Time-assisted Spatial MSA for 3D Human Pose Estimation**
	Sheng Lu, Qiyun Dong, Zhenyin Zhang, Gengsheng Chen,Yinna Zhu, Wei Xu *(Fudan University, China; Jiashan Fudan Institute, China)*
P1-33	**0173: SALTS: An Efficient and Flexible Self-Attention Accelerator with Long Token Support on FPGA**
	Kaiqi Chen, Xinhua Shi, Jun Han *(Fudan University, China)*
P1-34	**0222: RISC-V Neural Network Instruction Design and Simulation with Cache Scheduling via ROCC Interface**
	Siyao Dai, Zikang Zhou, Jun Han *(Fudan University, China)*
P1-35	**0226: Impact of external magnetic interference on the performance of MRAM-based neuromorphic computing**
	Yingtong He, Suihuan An, Yu Chen, Xue Zhou, Xihui Yuan, Weidong Zhang, Zheng Chai, Tai Min *(Xi'an Jiaotong University, China; Liverpool John Moores University, UK)*
P1-36	**0375: A Hardware Accelerator for Image Super Resolution with Algorithm Lightweighting and Custom Fusion Engine**
	Menghan Li, Sheng Lu, Jun Han *(Fudan University, China)*
P1-37	**0268: Hardware Implementation of High Speed Fault Tolerant Parallel Accelerator**
	Wenzhe Ma, Wenzhe Ma *(Fudan University, China)*
P1-38	**0349: Composite Filter-based Bicubic Interpolation Method and FPGA Implementation**

	Li Zhang, Jingjing Liu, Yujie Zhu, Jianhua Zhang *(Shanghai University, China)*
P1-39	**0157: MTJ based Temperature Tracking Read/Write Assist for High Speed SRAM Bitcell**
	Yongliang Zhou, Chengxing Dai, Jingxue Zhong, Yingxue Sun, Xin Li, Chunyu Peng *(Anhui University, China; Anhui Anxin Electronic Technology Co., Ltd, China）*
P1-40	**0353: System-level Evaluation of AOS Gain Cell eDRAMs for Low-power Normally-off Computing**
	Long Chen, Yecheng Yang, Wei Li, and Shao Hao Wang *(Fuzhou University, China)*
P1-41	**0024: A High Sigma Monte Carlo Analysis Solution Via Machine Learning for SRAM Margin Signoff**
	Amy Rao *(EBA Center, China)*
P1-42	**0138: Enhanced Multi-bit Computation using CIM SRAM Technology**
	Ruiyong Zhao, Yibo Hu, Zhipeng Ren, Yizhe Yin, Jing Chen *(Shanghai Institute of Microsystem and Information Technology, China)*
P1-43	**0045: A Compute-in-Memory Macro Based on Complementary 2T2C FeRAM Cell for BNNs**
	Jinyu Li, Mingzhang Xie, Shisheng Xiong *(Fudan University, China; China Resources Microelectronics Co., Ltd., China)*
P1-44	**0057: A Novel High Speed Low Power Differential Circuit-Based FRAM Read Scheme**
	Qiuyu Tao, Jiabao Ye, Xuecheng Cui, Nan Jiang, Jiangtao Cao, Xibo Chen, Jiuren Zhou, Bing Chen, Genquan Han *(Zhejiang University, China; Xidian University, China)*
P1-45	**0023: A 13-bit,1 MS/s Cyclic ADC, for high-speed CMOS Image sensor**
	Qi Lv, Rensheng Shen, Yu Cheng, Guoqiang Zhong, Yang Qu, Yuchun *(Dalian University of Technology, China)*
P1-46	**0110: An Area-Efficient 16-bit Four-channel R-2R DAC Based on Switching On-resistance Adaptive Calibration Technique**
	Kejun Wu, Yuchen Liu, Yuhan Hu, Yu He, Zhen Yu, Ning Ning *(University of Electronic Science and Technology of China, China)*
P1-47	**0215: A Background Calibration Method of Bandwidth Mismatch for Time-Interleaved ADCs Based on Neural Network**
	Tianqi Yang, Longsheng Wang, Xin Zhao, Shubin Liu, Dengquan Li, Zhangming Zhu *(Xidian University, China)*

P1-48	**0352: A Second-Order Dual-Charge-Pump Passive Noise Shaping SAR ADC for Medical Implant Devices**
	Kangkang Sun, Xuanxin Ke, Haoning Sun, Yuchen Wang, Feng Yan, Jingjing Liu *(Sun Yat-Sen University, China)*
P1-49	**0084: A 114.4-dB DR, 26-kHz BW Discrete-Time Incremental Zoom ADC**
	Yuanhong Ding, Longjiang Jia, Jian Mei, Lei Deng, Rui Yin *(Fudan University,China; National Integrated Circuit Innovation Center, China; Jiashan Fudan Institute, China)*
P1-50	**0087: A High-Resolution Low-Power Extended-Range Incremental ΣΔ ADC For Battery Management System**
	Long Zhang, Quan Sun, Rui Liu, Zhe Zheng, Jingjing Zhang, Haitao Liu *(Xi'an Aerosemi Technology Company Ltd., China; Beijing Smart-Chip Microelectronics Technology Company Ltd., China)*
P1-51	**0155: An Infrared AFE Chip and System with Non Invasive Blood Glucose Detection Output**
	Bin Li, Jiyuan Guo, Chengzhen Xie, Jian Mei, Lei Deng, Rui Yin *(Fudan University,China; National Integrated Circuit Innovation Center, China; Jiashan Fudan Institute, China)*
P1-52	**0183: A 12-Bit 8Gs/s Time-Interleaved Pipeline-SAR ADC with Calibration** Jie Pu, Jinda Yang, Jianwen Li, Rong Han, Xing Zhu, Lei Chen *(Chengdu Sino Microelectronics Technology Co., Ltd., China）*
P1-53	**0238: An Ultra-High Frame Rate ROIC for Hyperspectral Detection**
	Angyang Li, Ningning Li, Jian Mei, Lei Deng, Rui Yin *(National Integrated Circuit Innovation Center, China; Jiashan Fudan Institute, China; Fudan University, China)*
P1-54	**0273: A Background Digital Calibration Method for DTCs Used in Digital PLL Employing Dual-Path DTC**
	Renxuan Li, Xiaoyu Shan, Li Wang, Ang Hu, Dongsheng Liu *(Huazhong University of Science and Technology, China)*
P1-55	**0310: A High-Precision Sigma-Delta ADC for Battery Management System**
	Hao Xue, Liji Wu, Jing Hu, Zhiwei Li, Xiangmin Zhang *(Heilongjiang University, China: Tsinghua University, China: Beijing National Research Center for Information Science and Technology, China)*
P1-56	**0413: Multi-Sampling Mode CDAC Design for a 12-bit 200MS/s Pipelined-SAR ADC**
	Tianyu Zhang, Fan Ye, Shunli Ma *(Fudan University, China)*

P1-57	**0356: DSP-PUF: A Software PUF Based on Digital Signal Processor for IoT Security**
	Tengfei Yuan, Pengjun Wang, Yuejun Zhang, Mingze Ren, Shuang Hu *(Ningbo University, China; Wenzhou University, China)*
P1-58	**0042: A Q/V Band 49.6-54.5GHz,3.53dB NF,45dB Gain,2.09° Phase Error,2-Way Phased-Array Receiver for Satellite Application**
	Congrui Li, Qi Zhao, Ruolan Chen, Shulan Chen, Yan Wang, Lei Zhang *(Tsinghua University, China)*
P1-59	**0265: A Fractional-N SPLL Using Space-time Averaging and Phase Interpolator for Quantization Noise Reduction**
	Shengxiang Liu, Ke Sun, Chengyu Yang, Dongsheng Liu, Ang Hu *(University of Science and Technology, China)*
P1-60	**0342: A 47 μW Wake-Up Receiver With -77dBm Sensitivity Using a Mixer-First Architecture**
	Weitao He, Yaxin Zeng, Bin Jia, Hao Min, Hao Xu, Na Yan *(Fudan University, China; EPIC MEMS Corporation, China)*
P1-61	**0346: A Ka-Band CMOS Broadband Power Amplifier with 35.3% PAE for SATCOM Applications**
	Zhiqing Liu, Yu Chu, Yuting Sun *(Southwest China Institute of Electronic Technology, China)*
P1-62	**0348: RF Front-End Chip Design for Ku-Band with 130nm CMOS Technology**
	Huiquan Xie, Ziyu Wang, Tianrui Wang, Yifei Chen, Maliang Liu, Yintang Yang *(Xidian University, China)*
P1-63	**0394: Back-gate Bias Assisting VCRO Design**
	Chenglin Ye, Zheng Zhou, Xiaoyan Liu *(Peking University, China)*
P1-64	**0034: A 3.2-to-7.1GHz Quad-Core Dual-Mode Oscillator Achieving 193.6 dBc/Hz Peak FoM**
	Xiaoyu Shan, Renxuan Li, Mengming Zhang, Ang Hu, Dongsheng Liu *(Huazhong University of Science and Technology, China)*
P1-65	**0301: A 20.6 to 30.5 GHz Two Stage Cascode LNA in 40nm CMOS for Phase Array Tranceiver**
	Lei wang, Kefeng Han, Hao Xu, Rui Yin, Na Yan *(Fudan University, China; Jiashan Fudan Institute, China)*

P1-66	**0338: A 12-32 GHz Power Amplifier with 32-dBm Psat and 25% PAE in 0.15 μ m GaN**
	Xiangran Ni, Chunyue Bo, Tianyu Li, Qingyang Dong, Xin Jiang,Weijun Luo *(University of Chinese Academy of Sciences, China; Institute of Microelectronics of Chinese Academy of Sciences, China)*
P1-67	**0427: A Source-Driven Push-push Doubler with Wideband 2nd Harmonic Feedback**
	Yuyang Chen, Ao Zhang , Jianjun Gao, Jianjun Zhou *(Shanghai Jiao Tong University, China; Nantong University, China; East China Normal University, China)*
P1-68	**0367: Low Power Processor For IoT Device**
	Jincheng Li, Jiyuan Bai, Zelin Wang, GengSheng Chen, Xiaofang Zhou *(Fudan University, China; Jiashan Fudan Institute, China)*
P1-69	**0386: A Heterogeneous Integration System of Analog In Memory Computing and Field-Programmable Gate Array**
	Hua Chen, Yiming Qu, Wenhao Wu, Yi Zhao *(East China Normal University, China; China Nanhu Academy of Electronics and Information Technology, China; Zhejiang University, China)*

Thursday

Thursday, October 24, 9: 00 – 10: 30

Thursday, October 24, 9: 00 – 10: 30 Grand Ball Room
Keynote Session K3 Sheraton Zhuhai Hotel 1st Floor
Session Chair: Prof. Francois Rivet, University of Bordeaux, France

K3-1	**Integrated Circuit Innovation in the Age of AI**
9: 00 ~ 9: 45	Prof. Boris Murmann, University of Hawaii, USA
K3-2	**On-Chip ESD Protection: Methodologies, Challenges and Perspectives**
9: 45 ~10: 30	Prof. Albert Wang, University of California, Riverside, USA
	Coffee Break

Thursday, October 24, 10: 45 – 12: 15

Thursday, October 24, 10: 45 – 12: 15 Grand Ball Room
Panel Discussion Sheraton Zhuhai Hotel 1st Floor

Session Chair: Prof. Jianguo Yang, Zhangjiang Laboratory, China
Dr. Hailan Yi, Zhangjiang Laboratory, China

Opportunities and Challenges of Integrated Circuits in the AI Era

Tony Tae-Hyoung Kim *(Nanyang Technological University, Singapore)*, Xiaoyao Liang *(Shanghai Jiaotong University, China)*, Do Anh Tuan *(A*STAR, Singapore)*, Zhigang Ji *(Shanghai Jiaotong University, China)*, Chunmeng Dou *(University of Chinese Academy of Sciences, China)*

Thursday, October 24, 13: 30 – 15: 15

Thursday, October 24, 13: 30 – 15: 15 Meeting Room 1
Session A1: AI Circuit Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Yan Li, Fudan University, China

	Title
A1-1	**0468: Towards Efficient Computing Architecture and Chip For Embodied AI (invited)**
13:30 ~14:00	Hongbin Sun *(Xi'an Jiaotong University, China)*
A1-2	**0320: A High-Performance Multicore Testing Platform for Multi-Scenario Applications**
14:00 ~14:15	Zipeng Ling, Tianshu Zhuo, Zhuoyuan Yang, Jinhong Ye, JunHan, Jingtao Zhang *(State Key Laboratory of Integrated Chips and Systems, China; ZTE Corporation, China)*
A1-3	**0317: S-SIFT: A Simple SIFT Algorithm with High Efficiency**
14:15 ~14:30	Yixue Wang, Yujie Huang, Liyuan Peng, Mingyu Wang, Wenhong Li, Minge Jing, Xiaoyang Zeng *(Fudan University, China; Shanghai ExploreX Technology Co., Ltd., China)*
A1-4	**0345: Design of a High-Speed SAR Processor Based on the Chirp Scaling Algorithm**
14:30 ~14:45	Xianghe Cui, Yukun Song, Yurun Zhang, Jingyi Hu, Zhenmin Li, Duoli Zhang *(Hefei University of Technology, China)*
A1-5	**0196: Accelerating Matrix-Vector Multiplications of Large Language Models via Efficient Encoding**
14:45 ~15:00	Yongjin Tao, Wendi Sun, Song Chen, Yi Kang *(University of Science and Technology of China, China)*
A1-6	**0397: Flexible yet Efficient Transformer Acceleration with Unified Sparse Attention Support on FPGA**
15:00 ~15:15	Linfeng Zhong, Qingyu Guo, Runsheng Wang, Yuan Wang, Meng Li *(Peking University, China)*

Thursday, October 24, 13: 30 – 15: 15 Meeting Room 2
Session B3: Analog Circuit III Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Yubin Zhao, Sun Yat-sen University, China

	Title
B3-1	**0296: Beyond Bandwidth Trade-off: Simultaneous Wireless Power and Data Transfer System Design for Biomedical Implants (Invited)**
13:30 ~14:00	Quanrong Zhuang, Junyi Sun, Xusheng Zhang, Bo Li, Yi Shi, Hao Qiu *(Nanjing University, China)*
B3-2	**0011: A High Precision Operational Amplifier with Improved Bias Current Cancellation Circuit**
14:00 ~14:15	Zhili Zhang, Siyuan Yao, Hailong Wei *(Xi'an Microelectronics Technology Research Institute, China)*
B3-3	**0030: A 0.11-pJ/bit True Random Number Generator Based on a Clocked Current-Starved Inverter**
14:15 ~14:30	Kai Cheng, Chaowei Yang, Rui P. Martins, Pui-In Mak, Yong Chen *(University of Macau, Macao, China; Universidade de Lisboa, Portugal; Tsinghua University, China)*
B3-4	**0256: A Super-Mixed Current Decay Mode for Reducing the Angular Position Error in Stepper Motor**
14:30 ~14:45	Jian Fang, XuruiChen, Huajie Liu, Yuhan Jin *(University of Electronic Science and Technology of China, China)*
B3-5	**0065: A 109 dB 44-pArms Current Readout Circuit with Automatic Current Control for Multimodality Electrochemical Sensing**
14:45 ~15:00	Lina Wang, Jianzheng Li, Weiming Hu, Yajie Qin *(Fudan University, China)*
B3-6	**0080: A Low Temperature Coefficient Bandgap Reference For Temperature Sensor System**
15:00 ~15:15	Longjiang Jia, Yuanhong Ding, Jian Mei, Lei Deng, Rui Yin *(Fudan University, China; National Integrated Circuit Innovation Center, China; Jiashan Fudan Institute, China)*

Thursday, October 24, 13: 30 – 15: 15 Meeting Room 3
Session C3: RF Circuit I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Gengzhen Qi, Sun Yat-sen University, China

	Title
C3-1	**0075: High-Efficiency Power Amplifier Design for Bluetooth Low Energy Applications (invited)**
13:30 ~14:00	Bharatha Kumar Thangarasu, Li Shuai, Yu Hongshi, Ge Wansi, Liu Yuqing, Nagarajan Mahalingam, Meng Fanyi, Kaixue Ma, Juin J. Liou, Bo Wang, Younan Hua, Xiaomin

	Li, Lu Zhenghao, and Kiat Seng Yeo *(Tianjin University, China; North Minzu University, China; Singapore University of Technology and Design, Singapore; Wintech Nano-Technology Services Pte Ltd, Singapore; Soochow University, China)*
C3-2	**0104: A 0.15-6.5GHz Stacked CMOS Power Amplifier With Low-Frequency Bandwidth Extension**
14:00 ~14:15	Shijiao Dong, Wei Li, Xingyu Ma, Fan Chen, Hongtao Xu *(Fudan University, China)*
C3-3	**0269: A 2-to-2.7GHz Class-G Switched-Capacitor PA with Cascode Switch-Reused Structure Achieving 25.92dBm Peak Power and 42% Efficiency**
14:15 ~14:30	Jie Deng, Gengzhen Qi *(Sun Yat-sen University, China)*
C3-4	**0294: A X-band High Linearity Tunable Bandpass Filter in 130nm CMOS**
14:30 ~14:45	Tianrui Wang, Ziyu Wang, Huiquan Xie, Yifei Chen, Haokun Lan, Maliang Liu, Yintang Yang *(Xidian University, China)*
C3-5	**0302: Analysis of Polar and Quadrature Digital Transmitters for Wi-Fi7 Applications**
14:45 ~15:00	Lixuan Cao, Yun Yin *(Fudan University, China)*

Thursday, October 24, 13: 30 – 15: 15 Meeting Room 4

Session D3: Novel Device III Sheraton Zhuhai Hotel 2nd Floor

Session Chair: Prof. Mansun Chan, The Hong Kong University of Science and Technology, China

	Title
D3-1	**0458:Low-Dimensional Materials Enabled Wearable Circuits With Multi-level Detection and Wireless Communication Modules (invited)**
13:30 ~14:00	Li Tao *(Southeast University, China)*
D3-2	**0461: Memristive Circuits Based on Two-dimensional Layered Hexagonal Boron Nitride for Radiofrequency Applications (invited)**
14:00 ~14:30	Sebastian Pazos *(King Abdullah University of Science and Technology, Saudi Arabia)*
D3-3	**0452: Two-dimensional Ferroelectricity: Polarization Modulation and New Device (Invited)**
14:30	Fucai Liu *(University of Electronic Science and Technology of China, China)*

~15:00	
D3-4	**0475: Impact of Gate Overlap Length Modulation on Electrical Characteristics and Subthreshold Swing in Nanosheet TFETs with Varying Tunneling Mechanisms**
15:00 ~15:15	Zheng-Hong Zhong, Wei-Heng Tai, Jyi-Tsong Lin *(Sun Yat-Sen University, Taiwan, China)*

Thursday, October 24, 13: 30 – 15: 15 Meeting Room 5
Session E3: Power Device III Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Bo Zhang, University of Electronic Science and Technology of China (UESTC), China

	Title
E3-1	**0255: The Status of WBG Devices Towards Net-Zero Solutions (invited)**
13:30 ~14:00	Mikael Östling（*KTH Royal Institute of Technology, Sweden*）
E3-2	**0121: Impact of the Resistive Silicon Base Wafer on Substrate Coupling in Power Integrated Circuits in GaN-on-Si Technology**
14:00 ~14:15	Zijin Jiang, Rui (Ray) Yao, Miao Cui, Zhao Wang, Sang Lam, Stephen Taylor *(Xi'an Jiaotong-Liverpool University, China; The University of Liverpool, UK)*
E3-3	**0128: A Novel Snapback-free Double-RESURF Reverse conducting LIGBT with Dual Conduction Paths**
14:15 ~14:30	Yun Xia, Yuxi Wan, Wei Zeng, Yu Shi, Xiaoping Wang, Wei Liu, Haizhao Zhi, Ziwei Zhou, Xixi Luo, Ruize Sun, Xiaoming Wang, Yan Wang, Wanjun Chen *(Shenzhen Pinghu Laboratory, China; University of Electronic Science and Technology of China, China; Tsinghua University, China)*
E3-4	**0168: Comparsion of SiC Planar and Trench Junction Barrier Schottky Diode With Surge Current Capability**
14:30 ~14:45	Ziming Zhao, Yancong Liu, Hao Yuan, Fengyu Du, Yu Zhou, Keyu Liu, Xiaoyan Tang, Qinwen Song, Yuming Zhang *(Xidian University, China)*

Thursday, October 24, 13: 30 – 15: 15 Meeting Room 6
Session F3: Memory Device III Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Anquan Jiang, Fudan University, China

	Title
F3-1	**0266: Simulation of Program/Erase Cycling and Retention Loss in 3-D CTF**

	NAND Flash (invited)
13:30 ~14:00	Anuj Kumar, Ravi Tiwari, Souvik Mahapatra *(Indian Institute of Technology Bombay, India)*
F3-2	**0275: Switch-off Mechanisms in GeAsTe Ovonic Threshold Switching Selector Device (invited)**
14:00 ~14:30	Zeyu Hu, Zheng Chai, Weidong Zhang, Jianfu Zhang *(Liverpool John Moores University, UK; Xi'an Jiaotong University, China)*
F3-3	**0334: Orthorhombic-I (Pbca) Phase: Origin of Antiferroelectricity in HfZrO Films (invited)**
14:30 ~15:00	Wei Liu, Zeping Weng, Jianguo Li, Wenchao Yan, Yiming Qu, Yi Zhao *(Zhejiang University, China; East China Normal University, China)*
F3-4	**0254: The Maximum Storage Capacity of Open-loop Written RRAM is Around 4 Bits**
15:00 ~15:15	Yongxiang Li, Shiqing Wang, Zhong Sun *(Peking University,China)*

Thursday, October 24, 15: 30 – 17: 15

Thursday, October 24, 15: 30 – 17: 15 Meeting Room 1
Session A2: Security Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Yan Li, Fudan University, China

	Title
A2-1	**0469: Hardware Security Linking Everything: from Lightweight PUF to Post-Quantum Cryptography Hardware (invited)**
15:30 ~16:00	Yijun Cui, Jiang Li, Jiansheng Chen, Fei Lyu, Chenghua Wang, Weiqiang Liu *(Nanjing University of Aeronautics and Astronautics, China)*
A2-2	**0219: Backward-edge Control Flow Integrity based on Return Address Encryption**
16:00 ~16:15	Fengshuo Tian, Kaixuan Wang, Jun Han *(Fudan University, China)*
A2-3	**0239: Stability Enhancement Technique for Monostable PUF Based on Hysteresis Effect of Schmitt Trigger**
16:15 ~16:30	Ruize Xu, Gang Li, Pengjun Wang, Hui Li, Xudong Wu *(Wenzhou University, China)*

A2-4	**0355: A Reliable Current Starved Inverter PUF Based on SRAM Memory Structure**
16:30 ~16:45	Mingze Ren, Pengjun Wang, Yuejun Zhang, Shutong Zhang, Zhikang Chen, Tengfei Yuan *(Ningbo University, China; Wenzhou University, China)*
A2-5	**0354: An Efficient Convolutional Neural Network Hardware IP for Epilepsy Detection**
16:45 ~17:00	Yi Gong, Yuejun Zhang, Jiangtao Tu, Rongxin Zou, Liang Wen *(Ningbo University, China)*
A2-6	**0082: TLBshield: A Low-cost Secure Reinforce on Translation Lookaside Buffer to Mitigate the Speculative Attacks**
17:00 ~17:15	Yuyang Liu, Runye Ding, Yujie Chen, Pujin Xie, Yao Liu, Zhiyi Yu *(Sun Yat-sen University, China)*

Thursday, October 24, 15: 30 – 17: 15 Meeting Room 2
Session B4: Mixed Signal I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Yubin Zhao, Sun Yat-sen University, China

	Title
B4-1	**0038: Toward Unification of Digital Error Correction Algorithms for ADCs with Redundancy (invited)**
15:30 ~16:00	Haruo Kobayashi, Tomohiko Ogawa, Yutaro Kobayashi, Kentaroh Katoh, Jiangling Wei *(Gunma University, Japan; Fukuoka University, Japan; Yibin University, China)*
B4-2	**0482: A 1.2-V 2-GS/s Trimming-Free Input Buffer with Robust Output Common-mode Voltage (invited)**
16:00 ~16:30	Wei Zhang, Xizhu Peng, He Tang *(UESTC, China)*
B4-3	**0260: A 12-bit 1-MS/s SAR ADC Using V_{cm}-based Split MSB Switching and Segmented CDAC**
16:30 ~16:45	Zheng-Han Chen, Ya-Cong Zhang, Wen-Gao Lu, Zhong-Jian Chen *(Peking University, China)*
B4-4	**0333: A Simplified and Accelerated Opportunistic Bit Weight Calibration for High-Resolution ADCs**
16:45 ~17:00	Bingbing Ma, Wei Li, Hongtao Xu *(Fudan University, China)*
B4-5	**0154: Background Calibration for Bit Weights in Pipelined SAR ADCs Using Split ADC Architecture**

17:00 ~17:15	Zecheng Zhou, Longsheng Wang, Dongxian Ye, Yexin Zhu, Dengquan Li, Zhangming Zhu *(Xidian University, China)*

Thursday, October 24, 15: 30 – 17: 15 Meeting Room 3
Session C4: Sensor and MEMS I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Luda Wang, Peking University, China
Prof. Fan Ye, Fudan University, China

	Title
C4-1	**0108: Atomically Thin Graphene Nanopore based MEMS Iontronic Devices for Sensing, Separation and Energy Applications (invited)**
15:30 ~16:00	Luda Wang, Ruiyang Song, Ningran Wu *(Peking University, China)*
C4-2	**0132: Smart Vision Chip (invited)**
16:00 ~16:30	Liyuan Liu *(Institute Of Semiconductors, Chinese Academy Of Sciences, China)*
C4-3	**0191: Systems-on-Chips for Invasive Brain-Computer Interfaces: Challenges and Opportunities (invited)**
16:30 ~17:00	Jie Yang, Mohamad Sawan*(Westlake University, China)*
C4-4	**0410: Multi-physics Simulation and Application of Ion Gel Based Triboelectric Nanogenerators**
17:00 ~17:15	Chen Liu, Ruibo Wang, Ruiyi Gao, Yuming Zhang *(Xidian University, China; Air Force Engineering University, China)*

Thursday, October 24, 15: 30 – 17: 15 Meeting Room 4
Session D4: Novel Device IV Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Zhigang Ji, Shanghai Jiaotong University, China

	Title
D4-1	**0264: Recent Progress in the Development of Complementary Field-Effect Transistors (invited)**
15:30 ~15:57	Mansun Chan, Shengdong Zhang *(The Hong Kong University of Science and Technology, Hong Kong, China; Peking University, China)*
D4-2	**0471: SC-CMOS: Revolutionizing Semiconductor Technology with High Electron Mobility Materials and Advanced Node Optimization (invited)**

15:57 ~16:24	Jyi-Tsong Lin *(Sun Yat-Sen University, Taiwan, China)*
D4-3	**0074: Metal-Oxide Thin-Film Transistors for Artificial Neural Networks (invited)**
16:24 ~16:51	Yushen Hu, Tengteng Lei and Man Wong *(The Hong Kong University of Science and Technology, Hong Kong, China)*
D4-4	**0203: Cryogenic Threshold Voltage and On-current Variability Analysis of GAA Nanosheet FETs at 4K**
16:51 ~17:03	Zihao Liu, Tomoko Mizutani, Kiyoshi Takeuchi, Takuya Saraya, Hiroshi Oka, Takahiro Mori, Masaharu Kobayashi1 , Toshiro Hiramoto *(The University of Tokyo, Japan; National Institute of Advanced Industrial Science and Technology (AIST), Japan)*
D4-5	**0188: Reverse-Biased PN Junction Isolation for Leakage Suppression and Strain Enhancement in Gate-All Around Nanosheet FETs**
17:03 ~17:15	Boqian Shen, Chunlei Wu, Yumin Xu, Fei Zhao, Hanzhi Gu, Jian Ma, Yueyuan Yu, Yiming Xia, Qingqing Sun, David Wei Zhang *(Fudan University, China; Shanghai Integrated Manufacturing Innovation Center Co., Ltd, China; Jiashan Fudan Institute, China)*

Thursday, October 24, 15: 30 – 17: 15 Meeting Room 5
Session E4: Power Device IV Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Wentong Zhang, University of Electronic Science and Technology of China (UESTC), China

	Title
E4-1	**0405: Comparative Study on Reliability of Conventional SiC MOSFET and JBS Integrated SiC MOSFET (invited)**
15:30 ~16:00	Moufu Kong, Shurui Li, Hongfei Deng, Bo Yi, Hongqiang Yang, Sen Gong *(University of Electronic Science and Technology of China, China)*
E4-2	**0445: Study on Single Event Effect of SiC MOSFET by Proton Irradiation**
16:00 ~16:15	Wende Huang, Chengwen Fu, Yao Ma, Mingmin Huang , Xiaoping Dong, Qiang Yu *(Sichuan University, China; Sichuan Suining Lippxin Microelectronics Co., Ltd, China)*
E4-3	**0341: Investigating Single-Event Burnout in 4H-SiC Inverters: Experiments and Simulations**
16:15 ~16:30	Yong Gu, Yurui Yang, Hongyang Wen, Xiangyu Hou, Runhua Huang, Ao Liu, Bai Song, Jie Ma, Siayang Liu, Long Zhang, Weifeng Sun *(Southeast University, Nanjing China; Nanjing Electronic Device Institute, China)*

Thursday, October 24, 15: 30 – 17: 15 Meeting Room 6
Session F4: Memory Device IV Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Anquan Jiang, Fudan University, China

	Title
F4-1	**DRAM Evolution and Challenges （Invited）**
15:30 ~16:00	Robert Liu *(CXMT Corporation, China)*
F4-2	**0335: Phase-Change Materials and Their Applications (invited)**
16:00 ~16:25	You Yin *(Gunma University, Japan)*
F4-3	**0449: High-Density and High-Reliability RRAM for Memory and Computing Applications (invited)**
16:25 ~16:50	Yimao Cai, Xiahong Zhou, Zongwei Wang, Lin Bao, Ling Liang, Cuimei Wang, Ru Huang *(Peking University, China)*
F4-4	**0343: Impact of Different MAC Schemes on Computing In Memory based on 1T1R Array**
16:50 ~17:03	Ruiqing Xie, Gaoqi Yang, Zongwei Wang, Linbo Shan, Jinshan Li, Chaoyi Ban, Lin Bao, Ling Liang, Cuimei Wang, Yimao Cai, Ru Huang *(Peking University, China)*
F4-5	**0426: Investigation of Gate Injection Charges Behavior on FeFETs with $TiN/Al_2O_3/Hf_{0.5}Zr_{0.5}O_2/SiON/Si$ Structure by Analyzing ISPP/ISPE**
17:03 ~17:15	Jia Yang, Runhao Han, Saifei Dai, Tao Hu, Xianzhou Shao, Kanyi Li, Wenbo Fan, Xiaoqing Sun, Junshuai Chai, Hao Xu, Kai Han, Xiaolei Wang, Wenwu Wang, Tianchun Ye *(Key Laboratory of Fabrication Technologies for Integrated Circuits, Chinese Academy of Sciences, China; Institute of Microelectronics, Chinese Academy of Sciences, China; University of Chinese Academy of Sciences, China; Weifang University, China)*

Thursday, October 24, 17: 30 - 18：30

Thursday, October 24, 17: 30 – 18：30
Poster Session II Sheraton Zhuhai Hotel 1st Floor

	Title
P2-1	**0071: Assembly of Oxidized/Intrinsic 2D MXene Film for Improved Absorption Electromagnetic Shielding**

	Yulin Guo, Siteng Li, Jiafeng Song, Yilin Sun, Zhifang Liu, Weijia Luo *(Beijing Institute of Technology, China; Tsinghua University, China)*
P2-2	**0149: Semimetal Alloy Contact with Low Resistivity and Enhanced Thermal Budget for MoS_2 FETs**
	Kwok-Ho WONG, Mansun CHAN *(The Hong Kong University of Science and Technology, Hong Kong, China)*
P2-3	**0243: Copper Ion Migration in van der Waals $CuInP_2S_6$ Devices with Vertical and Lateral Structures**
	Jie Li, Yirong Guo, Pengying Chang *(Beijing University of Technology, China)*
P2-4	**0382: Edge-Dependent of Threshold Voltage in MoS_2 Nanoribbon-Based 2D FETs**
	Zhirong Peng, Mansun Chan *(The Hong Kong University of Science and Technology, Hong Kong, China)*
P2-5	**0438: Effect of Layer Thickness on the Transport Properties of ALD-deposited ZnO/In_2O_3 Heterojunction Thin-film Transistors**
	Zhenwei Li, Tiaoyang Li *(Fuzhou University, China)*
P2-6	**0417: Foundamentals of Low-Resistive Indium-Violet Phosphorene Top Contact: an ab-initio NEGF Study**
	Huaipeng Wang, Sicheng Liu, Shuaihong Li, Zhifang Liu, Yilin Sun, Jianlong Xu, Dan Xie *(Tsinghua University, China; Beijing Institute of Technology, China; Soochow University, China)*
P2-7	**0095: Broadband Photodetectors Based on Graphene/Perovskite Hybrid Structure with Ferroelectric Gating**
	Zhongyang Liu, Shuangqi Dong, Mingjie Li, Huaipeng Wang, Dan Xie, Yilin Sun *(Beijing Institute of Technology, China; Tsinghua University, China)*
P2-8	**0363: Interconnection Design of Chiplet Technology**
	Ning Chen, Chang Wu *(Fudan University, China)*
P2-9	**0102: Effects and Modeling Study on FDSOI MOSFETs at Cryogenic Temperature**
	Zhipeng Ren, Yibo Hu, Yizhe Yin, Ruiyong Zhao, Jing Chen *(Shanghai Institute of Microsystem and Information Technology, China)*
P2-10	**0247: Improved Channel Width and Morphology of Epi Silicon FinFET via Low Thermal Budgets Fin Thinning Technology**
	Peng Wang, Yupeng Lu, Guanqiao Sang, Renjie Jiang, Lei Cao, QingKun Li, Lianlian Li, hang zhang, zhonrui wang, meihe zhang, Qingzhu Zhang, Junfeng Li; Huaxiang

	Yin *(Institute of Microelectronics, China; University of Chinese Academy of Sciences, China)*
P2-11	**0364: Deep Investigation into Variability of Complementary Dopant Segregated Tunneling FET Based on Foundry Platform**
	Rundong Jia, Jianfeng Hang, Kaifeng Wang, Yongqin Wu, Hongyan Han2 , Ye Ren, Weihai Bu, Runsheng Wang, Qianqian Huang, Ru Huang *(Peking University, China; Seimiconductor Technology Innovation Center (Beijing), China)*
P2-12	**0379: Investigation of Common-Gate and Split-Gate Structures Based on CFET Standard Cells**
	Peishun Tang, Rongzheng Ding, Xiaona Zhu, Shaofeng Yu *(Fudan University, China)*
P2-13	**0187: Exploration of the effect of silver impurity on the minority carrier lifetime of semiconductor**
	Xin Tian, Peizhi Zhao, Yudong Li, Jun Xu, Tianling Ren *(Tsinghua University, China; Jiangsu Xinhua Semiconductor Technology Co., Ltd.; China)*
P2-14	**0267: Fabrication and Electrical Characterization of Mo/$Hf_xZr_{1-x}O_2$/Mo ferroelectric capacitors**
	Chunsheng Jiang, Wencai Du, Qin Xie *(Guangxi Normal University; China)*
P2-15	**0116: Effect of Cascade Current Density and Plating Time on TSV Filling Effect in DC Power Supply**
	Weifeng Chen, Lijuan Peng, Xiaohui Wang, Fangzhou Wang, Guojian Ding, Qi Feng, Ping Yu, Peng Zuo, Feng Liu, Jiang Ma, Yang Wang, Haiqiang Jia, Hong Chen *(Songshan Lake Materials Laboratory, China; Shenzhen University, China)*
P2-16	**0396: High-Performance Carbon Nanotube Optoelectronic Transistors for Memory Applications**
	Shuang Liu, Heyi Huang, Yanqing Li, Yadong Zhang, Feixiong Wang,Yupeng Lu, Renjie Jiang, Jiali Huo, Huaxiang Yin *(Institute of Microelectronics, China; University of Chinese Academy of Sciences, China)*
P2-17	**0414: Investigation of the channel width dependence of IGZO TFT by experiment and TCAD simulation**
	Yanyu Yang, Yupeng Lu, Shuang Liu，Renjie Jiang, Jie Luo, Yunjiao Bao, Peng Wang, Gaobo Xu,Huaxiang Yin *(Institute of Microelectronics, China; University of Chinese Academy of Sciences, China)*
P2-18	**0120: A Test and Evaluation Platform for Quantitative Analysis of High-Reliability Designs**
	Yifeng Huang, Wenqing Wan, Chang Wu *(Fudan University, China)*

P2-19	**0171: Hot-Carrier Injection Characterization of n-LDMOS Transistors and Stress Tests in a Buck Converter Configuration**
	Chun Yee Chu, Wai Tung Ng *(University of Toronto, Canada)*
P2-20	**0044: Automated Verification of Functional Interface Connections in Circuit Schematics**
	Keli Long, Xingyu Gao, Lei Li, Jinxiang Wang, Fangfa Fu, Liangquan Qiao, Jinghan Zhou *(Harbin Institute of Technology, China; 58th Research Institute of China Electronics Technology Group Corporation, China)*
P2-22	**0214: Co-Optimization Design Method of Temperature Variation and Circuit Aging in Digital Circuits**
	Songxuan He, Wangyong Chen, Ling Xiong, Linlin Cai *(Sun Yat-sen University, China)*
P2-23	**0308: Boolean Matrix Factorization Algorithm based on Error Shaping Technique and its Application on Approximate Logic Synthesis**
	Botao Xiong, Runhua Yang, Yuchun Chang *(Dalian University of Technology, China)*
P2-24	**0148: Automatically Device Sizing of Analog Circuit through Sequential Model-Based Optimization with Circuit Recognition**
	Shun-Qi DAI, Xiao WANG, Yuan LEI, Bei-Ping YAN *(Hong Kong Applied Science and Technology Research Institute (ASTRI), Hong Kong, China)*
P2-25	**0229: Vanadium Oxide-Based Artificial Synapses for Construction of Artificial Neural System**
	Zhuoling Zhou, Libin Liang, Hongzhi Chen, Changjiu Teng, Shilong Zhao, Wenjun Chen *(Foshan University, China)*
P2-26	**0048: High performance FeFET with α-IGZO Channel Enabled by Atomic-Layer-Deposited HfO_2 Interfacial Layer**
	Yinchi Liu, Hao Zhang, Xinlong Zhou, Dmitriy Anatolyevich Golosov, Chenjie Gu, Hongliang Lu, Shijin Ding, and Wenjun Liu *(Fudan University, China; Research Institute of Fudan University in Ningbo, China; Belarusian State University of Informatics and Radioelectronics, Republic of Belarus; Ningbo University, China; Zhangjiang Fudan International Innovation Center, China)*
P2-27	**0097: A Simulation Study on Cell Scaling Impacts in 3D Charge-trapping (CT) Flash Memory**
	Wanyu Li, Haitao Dong, Qianwen Wang, Yang Feng, Xuepeng Zhan, Jixuan Wu, Jiezhi Chen *(Shandong University, China; Qingdao University of Science & Technology, China)*

P2-28	**0390: Comprehensive Charaterizations on Read Disturbs in QLC Charge-Trap (CT) 3D NAND Flash**
	Shaoqi Yang, Xiaohuan Zhao, Peng Guo, Qianwen Wang, Guangkuo Yang1, Xinyi Guo, Pengpeng Sang, Jixuan Wu, Xuepeng Zhan, Jiezhi Chen *(Shandong University, China; Shandong Sinochip Semiconductors Co., Ltd, China; Qingdao University of Science & Technology, China)*
P2-29	**0047: Aspect Ratio Dependent Optimization and Comparison of Specific ON-Resistance of SJ and Hk MOSFETs with Extremely High Permittivity**
	Chenxing Wang, Zhentao Xiao, Zonghao Zhang, Zhenghao Jin, Zhiwan Liu, Zonglin Li, Zhemin An, Yunteng Jiang, Ruguan Li, Haimeng Huang, Hongqiang Yang *(University of Electronic Science and Technology of China, China; GRG Metrology & Test Group Co., Ltd., China)*
P2-30	**0092: Simulation Study of the Impact of Split Gate on SiC DTMOS Short Circuit Withstand Capability**
	Zixun Chen, Jinping Zhang, Yang Liu, Xudong Ma, Bo Zhang *(University of Electronic Science and Technology of China, China; Weihai Singa Electronics CO.LTD, China)*
P2-31	**0143: Improved Hall Mobility Measurement Distinguishing Interface Capturing Effect in 4H-SiC Inversion Channel**
	Xiangrui Fan, Hao Fu, Xinyu Zhang, Zilong Wu, Jiameng Sun, Jiaxing Wei, Siyang Liu, Weifeng Sun *(Southeast University, China)*
P2-32	**0163: A Superior SiC Lateral MOSFET with Patterned P-bury Layer Made on N-type Wafers**
	Xuke Yan, Junji Cheng, Xiaojun Fu, Bo Yi, Haimeng Huang, Hongqiang Yang *(University of Electronic Science and Technology of China, China; The 24th Research Institute of China Electronics Technology Group Corporation, China)*
P2-33	**0272: High Performance Termination Design and Fabrication For SiC MOSFET Device**
	Lei Huang, Junhou Cao, Chenlu Wang, Hao Fu, Jiaxing Wei, Siyang Liu, Weifeng Sun *(Southeast University, China)*
P2-34	**0422: Analysis of The Separation Degree For P-pillar in SiC Super-Junction Structure Through "Multiple Epitaxy-Ion Implantation" Route**
	Hao-Bo Kang, Hao Yuan, Feng-Yu Du, Yu Zhou, ke-Yu Liu, Xiao-Yan Tang, Chao Han, Qing-Wen Song, Yu-Ming Zhang *(Xidian University, China; The Xidian-Wuhu Research Institute, China)*

P2-35	**0433: Numerical Analysis of the CIBL Effect on ShortCircuit Characteristics of DG-CSTBTs with Reduced Mesa Width**
	Zhengyu Lang, Jinping Zhang, Shiwei Zheng, Shuyang Huang, Haonan Deng, and Bo Zhang *(University of Electronic Science and Technology of China, China; Nanjing SilverMicro Electronics, China)*
P2-36	**0441: Innovations in GaN HEMT Design: Achieving Superior Power Output and Thermal Management**
	Shiming Li, Bowen Yang, Mei Wu, Ling Yang, Bin Hou, Meng Zhang, Xiaohua Ma,Yue Hao *(Xidian University, China)*
P2-37	**0446: An Enhanced RC-IGBT Incorporating Superjunction and Discontinuous Field Stop Layers for Improved Efficiency**
	Yiming Jia, Jieyu Long, Zhiwei Jing, Haimeng Huang, Hongqiang Yang *(University of Electronic Science and Technology of China, China)*
P2-38	**0105: Simulation Study on 1200V CS-SemiSJ-IGBT for Reduced Switching Loss and Fast Switching**
	Luping Li, Zehong Li, Peng Chen, Yuzhou Wu, Qiansheng Rao, Ming Li, Haifeng Qin, Li Wan, Yang Yang, Wei Li, Min Ren *(University of Electronic Science and Technology of China (UESTC), China; China Resources Microelectronics (Chongqing) Ltd., China; Shanghai Super Semiconductor Technology Company Ltd., China)*
P2-39	**0194: A Dual-Gate Trigger Thyristor for Reducing the Probability of False Triggering**
	Pengcheng Xing, Qingbo Wan, Jie Huang, Ruize Sun, Chao Liu, Wanjun Chen *(University of Electronic Science and Technology of China (UESTC), China)*
P2-40	**0202: Ultra Fast Diode Avalanche Shaper with Floating Junction**
	Zhen Yang, Yu Zhou, Xiao-Yan Tang, Chao Han, Qing-Wen Song, Yu-Ming Zhang *(Xidian University, China; Xidian-Wuhu Research Institute, China)*
P2-41	**0204: Silicon Carbide Diode Avalanche Shaper with Multi-Point Quasi-Uniform Triggering**
	Lin Cheng, Yu Zhou, Xiao-Yan Tang, Chao Han, Yu-Ming Zhang, Qing-Wen Song *(Xidian University China; Xidian-Wuhu Research Institute, China)*
P2-42	**0209: Super Field Plate LIGBT with Improved Performance for Both Cell and Terminal Region**
	Weihao Lu, Jing Li, Jitong Wang, Chaoyang Peng, Chunwei Zhang *(University of Jinan, China)*

P2-43	**0248: High-temperature oxidation of 4H-SiC and gate oxide reliability dependence on oxidation temperature**
	Baoyan Feng, Xiaoyan Tang, Yi bo Zhang, Chao Han, Hao Yuan, Qing wen Song *(Xidian University, China; Xidian-Wuhu Research Institute, China)*
P2-44	**0299: Optimization for a High-voltage Recessed-gate β- Ga_2O_3 MOSFET by Gate and Drain Field Plate Technology**
	Bo Yi, Yuan Qiao, Ming Dai, Fan Xu, JunJi Cheng, HaiMeng Huang, MouFu Kong, XingLi Jiang, HongQiang Yang *(University of Electronic Science and Technology of China, China; Chongqing Institute of Microelectronics Industry Technology, China; Chengdu Semi-Future Technology Co., Ltd, China)*
P2-45	**0315: A Novel Voltage Sensor with Composite Trench Structure for High Voltage IGBT**
	Yang Yang, Ze-Hong Li, Senior Member, IEEE, Li-Hang Dong, Wei Li, Peng-Fei Jia, Zhi-Yu Yang, Li Wan, Yi-Shang Zhao, Tong-Yang Wang, Zi-Ming Xia *(University of Electronic Science and Technology of China, China; China Resources Microelectronics (Chongqing) Limited, China; Chongqing Institute of Microelectronics Industry Technology, China)*
P2-46	**0316: A Novel Triggered Voltage Sensing Structure for High Voltage IGBT**
	Yang Yang, Ze-Hong Li, Senior Member, IEEE, Li-Hang Dong, Wei Li, Peng-Fei Jia, Zhi-Yu Yang, Li Wan, Yi-Shang Zhao, Lu-Ping Li, Zi-Ming Xia, and Tong-Yang Wang *(China Resources Microelectronics (Chongqing) Limited, China; University of Electronic Science and Technology of China, China; Chongqing Institute of Microelectronics Industry Technology, China)*
P2-47	**0374: Investigation of Threshold Voltage Instability in p-GaN Gate HEMTs under Surge Current Stress**
	Xiaoming Wang, Yu Shi, Chunhua Zhou, Haizhao Zhi, Yun Xia, Ruize Sun, Xinghuan Chen, Wanjun Chen, Bo Zhang *(University of Electronic Science and Technology of China, China; Shenzhen Pinghu Laboratory, China; China Electronic Product Reliability and Environmental Testing Research Institute, China)*
P2-48	**0408: A Novel Ga_2O_3 High-k Trench MOSFET with Improved Forward and Reverse Performance**
	Moufu Kong, Lewei Lyu, Haoran Wang, Zhaoyu Ai, Xinyang Chen, Fanxin Meng, Qiang Hu *(University of Electronic Science and Technology of China, China; Chengdu High-tech Development Co.Ltd, China; Chengdu Semi-Future Technology Co. Ltd, China)*
P2-49	**0054: A Nonlinear Behavioral Modeling Approach for Microwave Transistors Considering ElectrothermalAging Degradation**
	Lin Cheng, Hongliang Lu, Silu Yan, Junjun Qi, Jiantao Qiao, Yuming Zhang *(Xidian*

	University, China)
P2-50	**0344: Electrical and Thermal Analysis of CNT nTSV Applied to BS-PDN: A Modeling Study**
	Kai Ying, Baohui Xu, Jie Liang *(Shanghai University, China)*
P2-51	**0357: A Unified Current-Voltage Compact Model for Organic Light-Emitting Diode**
	Wenbin Wang, Mingyu Ma,Wangjun Yang, Jianghao Ma, Hailong You, Cong Li *(Xidian University, China)*
P2-52	**0162: Threshold Voltage and Mobility Extraction of Negative Bias Temperature Instability in 22nm FD SOI MOSFETs**
	Yibo Hu, Hao Ge, Zhipeng Ren, Yizhe Yin, Ruiyong Zhao, Jing Chen *(Shanghai Institute of Microsystem and Information Technology, China)*
P2-53	**0428: Modeling of Silicon Single-Photon Avalanche Diodes for Process and Design Optimization**
	Jing Fu, Anran Guo, Hongbo Zhang, Guowei Li, Huaping Ma,Ruizhi Li, Yuwei Chen *(National Key Laboratory of Integrated Circuits and Microsystems, China; CETC No.44 Research Institute, China)*
P2-54	**0046: A Novel Modeling Method for BV Characteristics of ESD Protection Devices**
	Ke Zhang, Yang Wang, Xiangliang Jin *(Hunan Normal University, China; Peking University, China)*
P2-55	**0070: Analysis of The Impact of Parasitic Bipolar Amplification on Charge Sharing Based on Analytical Model**
	Yutao Zhang, Hongliang Lyu, Yuming Zhang, Ruxue Yao *(Xidian University, China)*
P2-56	**0123: Research on the performance degeneration of GGNMOS under total ionizing dose Radiation**
	Jiekai Feng, Ping Luo, Chengxin Li, Jiaxuan Hu, Peng Li, Pengfei Liao *(Univ. of Elec. Sci. and Technol. Of China, China; Chongqing Institute of Microelectronics Industry Technology, China; The 24th Research Institute of China Electronics Technology Group Corporation, China)*
P2-57	**0208: The UIS Withstand Capability and Device Failure Mechanism of 650 V p-GaN Gate HEMTs**
	Qihang Huang, Luanxi Ma, Shuting Huang, Yanning Nie, Jianggen Zhu, Yu Shi, Rongxin Du, David Zhou, Yuxi Wan, Bo Zhang, Qi Zhou *(University of Electronic Science and Technology of China, China; Shenzhen Pinghu Laboratory, China)*

P2-58	**0100: Time Dependent Dielectric Breakdown in n-MOSFETs Fabricated by Low-Temperature and Low-Pressure Mild Oxidation After Plasma Solidification**
	Qiao Teng, Yanning Chen, Fang Liu, Bo Wu, Yongfeng Deng, Dawei Gao *(Zhejiang University, China; Beijing Smart-chip Microelectronics Technology Co., Ltd, China)*
P2-59	**0228: Simulation of BTI for GAA MOSFETs with Enhanced Parameters Extraction**
	Yongjia Wang, Yijiao Wang, Shuhan Wang, Jinghan Xu, Xiaoyan Liu *(Peking University, China; Beihang University, China)*
P2-60	**0033: Gold Thermocompression Wafer Bonding for Quartz MEMS Applications**
	Ting Yang, Dongxiang Han, Jun Xu, Tian-Ling Ren *(Tsinghua University, China; Nanjing University of Aeronautics and Astronautics, China)*
P2-61	**0083: An Adaptive Threshold Analog Front-End Circuit for Direct ToF LiDAR**
	Jianping Guo, Xiaoyang Zeng, Wenhong Li, Mingyu Wang *(Fudan University, China)*
P2-62	**0184: Design of Ultra-Broadband Metamaterial Absorber from Infrared to Terahertz**
	Xiangze Liu, Wenbin Zhou, Tiantian Shi, Yiming Liao, Feng Yan, Xiaoli Ji *(Nanjing University, China; Nanjing University of Science and Technology, China)*
P2-63	**0398: Large modulation bandwidth Si-based avalanche photodiode for visible light communications**
	Jiabin Wu, Yidi Hu, Chiang Zhu, Zhichong Wang, Xiaona Zhu, Chao Shen *(Fudan University, China)*
P2-64	**0400: An Artificial Neuromuscular Synapse Based on a Ferroelectric $Pb(Zr_{1-x}Ti_x)O_3$/SiC Floating Gate Transistor**
	Yu Liu, Lin Lin, Xiang Wang, Chengyan Zhong, Junxiong Guo, Wen Huang, Yufeng Guo *(Nanjing University of Posts and Telecommunications, China; University of Electronic Science and Technology of China, China; Chengdu University, China)*

Friday

Friday, October 25, 9: 00 – 10: 30

Friday, October 25, 9: 00 – 10: 30 Grand Ball Room
Keynote Session K4 Sheraton Zhuhai Hotel 1st Floor
Session Chair: Prof. Rui Yin, National Integrated Circuit Innovavtion Center, China

K4-1	**CMOS Digital Radiography**
9: 00 ~9: 45	Prof. Youngcheol Chae, Yonsei University, Korea
K4-2	**High-Frequency and Wideband RF Filters for 6G and Wi-Fi 7**
9: 45 ~10: 30	Prof. Chengjie Zuo, University of Science and Technology of China, China
	Coffee Break

Friday, October 25, 10: 45– 12: 15

Friday, October 25, 10: 45 – 12: 15 Meeting Room 1
Session A3: Digital & Memory Circuit Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Fan Ye, Fudan University, China

	Title
A3-1	**0451: One-Step Circuit Analysis Based on LCA for Sparse Coding (invited)**
10:45 ~11:15	Hanxi Xu, Zirui Chen, Qi Chen, Xiangshui Miao, Yuhui He *(Huazhong University of Science and Technology, China; Politecnico di Milano, Italy)*
A3-2	**0369: A Hybrid-Logic Scheme for High-Performance and Low-Power Decoders in 7nm Process**
11:15 ~11:27	Donghao Xia, Yuejun Zhang, Mengfan Xu, Liang Wen, Yiting Guo *(Ningbo University, China; China Coast Guard Academy, China)*
A3-3	**0049: Ternary Logic Units Design Based on the TDDFETs**
11:27 ~11:39	Hua Qiang, Haoran Lu, Xiaotao Liu, Linlin Xing, Bin Lu *(Shanxi Normal University, China)*
A3-4	**0474: Enhancing SRAM Cell Stability Through Single-Carrier CMOS Latch Integration**
11:39 ~11:51	Yuan-Yu Chuang, Pei-Zhang Xie, and Jyi-Tsong Lin *(Sun Yat-Sen University, Taiwan, China)*
A3-5	**0199: An RRAM based 9T1R NVSRAM for Low-Power Computing in Memory**
11:51 ~12:03	Huimeng Guo, Yujia Li, Tingrui Ren, Chenge Dong, Liang Wang, Yuanfu Zhao, Yanlong Zhang *(Hangzhou Dianzi University, China; Beijing Microelectronics Technology Institute, China; Beihang University, China)*
A3-6	**0377: A High-Resistance SOT Device Based Computing In-Memory Macro with High Sensing Margin and Multi-Bit MAC Operations for AI Edge Inference**
12:03 ~12:15	Junzhan Liu, Jinyao Mi, Haiyan Qin, He Zhang, Wang Kang *(Beihang University, China; Hangzhou International Innovation Institute, China)*

Friday, October 25, 10: 45 – 12: 15 Meeting Room 2
Session B5: Mixed Signal II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Fan Ye, Fudan University, China

	Title
B5-1	**0167: When Time Interleaving encounters Oversampling in ADC (invited)**
10:45	Mingqiang Guo, Dongyang Jiang, Shulin Zhao, Sai-Weng Sin, Rui P. Martins

~11:15	*(University of Macau, Macao, China; Universidade de Lisboa, Portugal)*
B5-2	**0322: A 0.000355mm^2 4.6μm-Pitch 5.75fJ/Conv 6-bit SAR ADC for High Throughput Parallel Readout of Analog SRAM Computing-In-Memory**
11:15 ~11:30	Lin Wu, Lichen Feng, Hongwei Shan, Zhangming Zhu *(Xidian University, China)*
B5-3	**0051: A 250MS/s, 12 Bit Pipeline-SAR ADC Using Coarse-fine Ring Amplifier**
11:30 ~11:45	Linghao Liu, Junyan Ren, Fan Ye *(Fudan University, China)*
B5-4	**0420: A 0.71pJ/b 16Gb/s Equalizer with Inverter_based CTLE and 4-Tap Speculative DFE**
11:45 ~12:00	Huihong Zhang, Chuangao Yan, Luo Peng, Maliang Liu *(Xidian University, China)*
B5-5	**0350: A Digital Foreground Calibration Method for Pipeline SAR ADCs Using Extended Kalman Filter**
12:00 ~12:15	Dayan Zhou, Yuguo Xiang, Junyan Ren, Fan Ye *(Fudan University, China)*

Friday, October 25, 10: 45 – 12: 15 Meeting Room 3
Session C5: Sensor & MEMS II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Zhanfeng Huang, Sun Yat-sen University, China

	Title
C5-1	**0099: A Flexible Graphene Acoustic Sensor for Sound Signal Acquisition and Spike Neural Network Recognition**
10:45 ~11:00	Lu-Yu Zhao, Hao-Yuan Shen, Yi-Wen Wu, Lu-Lu Zhang, Yu-Tao Li, Tian-Ling Ren *(Beijing Institute of Technology, China; Beijing University of Chemical Technology, China; Tsinghua University, China)*
C5-2	**0058: 0.15μm BCD Platform with High Sensitivity Hall Device and Low Noise CMOS for Sensor IC Applications**
11:00 ~11:15	Guiqiang Zheng, Qingyin Zhong, Jie Ma, Nannan Cheng, Yichen Li, Yongjia Li, Siyang Liu, Xiaofeng Sun, Dejin Wang, Sen Zhang, Long Zhang, Weifeng Sun *(Southeast University, China; CSMC Technologies Corporation, China)*
C5-3	**0166: A High Dynamic Range Pixel with Inverse Proportional Response**
11:15 ~11:30	Yuchen Wang, Wenji Mo, Haoning Sun, Jingjing Liu *(Sun Yat-sen University, China)*

C5-4	**0285: Enhancement of Image Sensor Pixel Performance through Ring-Shaped Vertical Transfer Gate Structure**
11:30 ~11:45	Shuang Yan, Shuai Yuan, Haoping Zheng, Yudi Zhao, Gang Du, Junchen Dong, Kai Zhao *(Beijing Information Science and Technology University, China; Peking University, China; HT-tech Jiangsu Co., Ltd., China)*

Friday, October 25, 10: 45 – 12: 15 Meeting Room 4
Session D5: Process I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Heng Wu, Peking University, China

	Title
D5-1	**0251: Studies on Selective Deposition of SiO_2 by Rapid Atomic Layer Deposition (Invited)**
10:45 ~11:15	Sicong Shao, Jin Yan, Wang Li, Kun Cao , Rong Chen *(Huazhong University of Science and Technology, China)*
D5-2	**0404: Resistance Dependence of Cobalt on Line Width in Advanced Interconnects: First-Principles Modelling**
11:15 ~11:30	Kang Wang, Menglin Huang, Shiyou Chen *(Fudan University, China)*
D5-3	**0418: Reducation of Specific Contact Resistivity by Employing Pre-amorphization Implantation and In situ Steam Generation Oxidation**
11:30 ~11:45	Chang Liu, Xu Chen, Jinbiao Liu, Yanping He, Wenjuan Xiong, Weibing Liu, Mingshan Liu, Zhe Liu, Yaoqi Dong, Jeffrey Xu, Jing Xu, Jun Luo *(Institute of Microelectronics, Chinese Academy of Sciences, China. University of Chinese Academy of Sciences, China. Huawei Technologies Company limited, China)*
D5-4	**0432: High-performance Ultrathin ITO Thin Film Transistor With Ultralow Subthreshold Swing**
11:45 ~12:00	Yanheng Liu, Tiaoyang Li *(Fuzhou University, China)*

Friday, October 25, 10: 45 – 12: 15 Meeting Room 5
Session E5: Reliability I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Ming Xiao, Sun Yat-sen University, China

	Title
E5-1	**0453: Challenges of Design for Reliability in Advanced CMOS Technology: From Single-mode to Mixed-mode Mechanisms (invited)**

10:45 ~11:15	Zixuan Sun, Lining Zhang, Ru Huang, Runsheng Wang *(Peking University, China)*
E5-2	**0232: Frequency-dependent Time-dependent Dielectric Breakdown (TDDB) Behavior and Physical Study in Gate Oxides (invited)**
11:15 ~11:45	Wei Liu, Chu Yan, Xinwei Yu, Yiming Qu, Wenchao Yan, Yi Zhao *(Zhejiang University, China; East China Normal University, China; Zhejiang Li-ryder Technologies Co. LTD, China)*
E5-3	**0181: Lightning Protection Stacked TVS Structure Based on a Novel Total-Ionizing-Dose Radiation-hardened Technology**
11:45 ~12:00	Zhao Qi, Hongquan Chen, Yirui Jia, Nailong He, Zhili Zhang, Sen Zhang, Ming Qiao, Bo Zhang *(University of Electronic Science and Technology of China, China; CSMC Technologies Corporation, China)*

Friday, October 25, 10: 15 – 12: 15 Meeting Room 6
Session F5: Device Modeling I Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Ming Li, Peking University, China

	Title
F5-1	**0466: Electric-Thermal Characteristics of Bottom P-i-N Isolated Nanosheet Gate-All-Around FETs (invited)**
10:45 ~11:15	Chunlei Wu, Hanzhi Gu, Jian Ma, Boqian Shen, Fei Zhao, Yueyuan Yu, Yiming Xia, Qingqing Sun, David Wei Zhang *(Fudan University, China; Shanghai Integrated Menufacturing Innovation Center Co., Ltd, China; Jiashan Fudan Institute, China)*
F5-2	**0180: Surface Potential-Based Compact Model for ITO Thin-film Transistors with Ultra-thin Channel**
11:15 ~11:30	Wenting Xu, Xinxin Shen, Zuoxu Yu, Tingrui Huang, Yuzhen Zhang, Weifeng Sun, Wangran Wu *(Southeast University, China)*
F5-3	**0186: A Continuous Full Channel Potential Model for Accurate Synthetic Electricfield Calculating in Gate-All-Around Devices**
11:30 ~11:45	Fei Zhao, Chunlei Wu, Yumin Xu, Boqian Shen, Jian Ma, Hanzhi Gu, Yueyuan Yu, Yiming Xia, Qingqing Sun, David Wei Zhang *(Fudan University, China; Shanghai Integrated Manufacturing Innovation Center Co., Ltd, China; Jiashan Fudan Institute, China)*
F5-4	**0271: Deep Learning and Adaptive Pattern Search Based BSIM-CMG Parameter Extraction Applicable to Process Migration**
11:45 ~12:00	Xingyu Li, Wangyong Chen, Linlin Cai *(Sun Yat-sen University, China)*

Friday, October 25, 13: 30 – 15: 15

Friday, October 25, 13: 30 – 15: 15 Meeting Room 1
Session A4: Processor Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Yao Liu, Sun Yat-sen University, China

	Title
A4-1	**0201: RISC-V Domain-Specific Processor for Accelerating SPHINCS+ on Multi-Core Architecture**
13:30 ~13:45	Shengnan Zhang, Yifan Zhao, Xinglong Yu, Jun Han *(Fudan University, China)*
A4-2	**0113: Design of an Out-of-Order Superscalar Processor with Improved Register Alias Table Recovery Method**
13:45 ~14:00	Wu Yang, Jun Zhang *(Central South University, China)*
A4-3	**0220: An SDPF RISC-V Processor with Two-stage Pseudo-pipelined Architecture for IoT Applications**
14:00 ~14:15	Wenji Mo, Yuchen Wang, Haoning Sun, Jingjing Liu *(Sun Yat-sen University, China)*
A4-4	**0366: A Unified Verification Scheme for the Acceleration of RISC-V Processor Design**
14:15 ~14:30	Zixiang Chen, Jiyuan Bai, Yueru Yu, Gengsheng Chen, Xiaofang Zhou *(Fudan University, China; Jiashan Fudan Institute, Jiaxing, China)*
A4-5	**0370: Asynchronous Arbitration Circuit Optimization for Multicore Neuromorphic Processors**
14:30 ~14:45	Jiajie Guo, Guangyao Lin, Bohan Wang, Zhiyi Yu, Shanlin Xiao *(Sun Yat-sen University, China)*

Friday, October 25, 13: 30 – 15: 15 Meeting Room 2
Session B6: Mixed Signal III Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Shuo Li, Fudan University, China

	Title
B6-1	**0289: A Second-Order Charge Pump Noise-Shaping SAR ADC (invited)**
13:30	Haoning Sun, Yuchen Wang, Wenji Mo, Kangkang Sun, Jingjing Liu *(Sun Yat-Sen*

~14:00	*University, China)*
B6-2	**0385: Computing in Memory for Accelerating Light Weighted On-Chip Learning in IoT Devices (Invited)**
14:00 ~14:30	Zhiwang Guo, Xiaoyong Xue, Jun Han, Peng Zhou, Xiaoyang Zeng *(Fudan University, China; Shaoxin Laboratory, China)*
B6-3	**0314: A Novel Beamforing Receiver Archiercture Combining MASH SDM and BSP**
14:30 ~14:45	Tao Zhong, Yuekang Guo, Jing Jin, Jianjun Zhou *(Shanghai Jiao Tong University, China)*

Friday, October 25, 13: 30 – 15: 15 Meeting Room 3
Session C6: RF Circuit II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Gengzhen Qi, Sun Yat-sen University, China

	Title
C6-1	**0424: A 10-GHz Low Power Class-C VCO with Long-Term Reliability and Tunable Performance in 28 nm FD-SOI for Satellite Communications (invited)**
13:30 ~14:00	Yann Deval, Henrique Iha Taguti, Ayoub Ait Ihda, Herve Lapuyade, Stephane Rochette, Francois Rivet *(Univ. Bordeaux, France; Thales Alenia Space, France)*
C6-2	**0313: A 191-GHz Harmonic Oscillator with Self-Feeding Line and Return-Path Gap Coupler Structure in 65nm CMOS**
14:00 ~14:15	Xiaohan Shen, Chen Jiang *(Fudan University, China)*
C6-3	**0278: A Blocker-Tolerant High-Linear Receiver Employing Baseband Noise-Cancelling and Bottom-Plate Switched-Capacitor Techniques**
14:15 ~14:30	Chenxiang Cai, Gengzhen Qi *(Sun Yat-sen University, China)*
C6-4	**0412: A High Sensitivity Series-Parallel Rectifier with Pre-Bias for RF Energy Harvesting Systems**
14:30 ~14:45	HaiQin Wu, Dejian Li, Xin Jin, Xufeng Liao, Lianxi Liu *(Xidian University, China; Beijing Smart-Chip Microelectronic Technology Co., Ltd, China)*
C6-5	**0103: A 24.3-43.7 GHz Variable-Gain Low-Noise Amplifier With Phase Self-Compensation**
14:45 ~15:00	Yue Wu, Wei Li, Mohan Zhou, Hongtao Xu *(Fudan University, China)*

C6-6	**0480: A Broadband Active Variable Attenuator With Phase Compensation Technique**
15:00 ~15:15	Zhiying Xia, Zhiqun Li, Bofan Chen, Xiaowei Wang *(Southeast University, China)*

Friday, October 25, 13: 30 – 15: 15 Meeting Room 4
Session D6: Process II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Xiaona Zhu, Fudan University, China
Prof. Fan Ye, Fudan University, China

	Title
D6-1	**0456:Advanced Logic Devices' DTCO Beyond 3nm Process Technology Node (invited)**
13:30 ~14:00	Xiaona Zhu, Hongliang Lu, Shaofeng Yu, David Wei Zhang *(Fudan University, China)*
D6-2	**0483:Defect-Centric Insights into Flicker Noise in Ultra Scaled FETs: From Physics to Compact Model for Circuit Level Simulation (invited)**
14:00 ~14:30	Chenyang Zhang, Yu Xiao, Pengpeng Ren, Shiyu Xia, Sheng Yang, Lining Zhang, Runsheng Wang, Zhigang Ji *(Shanghai Jiaotong University, China; Peking University, China)*
D6-3	**0454: A Modified Virtual-Source Model for Ballistic Transport Characterization of FinFETs at Cryogenic Temperature**
14:30 ~14:45	Hongbo Wang, Zirui Wang, Zixuan Sun, Runsheng Wang, Ru Huang *(Peking University, China)*
D6-4	**0114: Investigation on Asymmetric HfO_2-ZrO_2-HfO_2 Superlattice Gate Stacks with Ultra-low EOT for Advanced Transistors**
14:45 ~15:00	Haiyuan Lyu, Kun Zhong, Zhaohao Zhang, Huaxiang Yin *(Institute of Microelectronics, Chinese Academy of Sciences, China; University of Chinese Academy of Sciences, China)*
D6-5	**0119: Evaluation of Contact Resistance with the 'L' Kelvin Test Structure and the Modified Kelvin Test Structure**
15:00 ~15:15	Gui Chen, Yun-Hao Shao, Xin-Ping Qu *(Fudan University, China)*

Friday, October 25, 13: 30 – 15: 15 Meeting Room 5
Session E6: Reliability II Sheraton Zhuhai Hotel 2nd Floor

Session Chair: Prof. Wangyong Chen, Sun Yat-sen University, China

	Title
E6-1	**0221: Characterization and Modeling of Non-conducting RF Hot Carrier Stress in FinFETs (invited)**
13:30 ~14:00	G. Niu, X. Ding, H. Zhang, W. Wang, K. Imura, F. Dai *(Auburn University, USA; Maxlinear Inc., USA)*
E6-2	**0164:Predictive Modelling of Hot Carrier Degradation (invited)**
14:00 ~14:30	James Brown , Kean Hong Tok , Rui Gao , Zhigang Ji , Weidong Zhang, Jian Fu Zhang *(Liverpool John Moores University, UK; No. 5 Electronics Research Institute of the Ministry of Industry and Information Technology, China; Shanghai Jiaotong University, China)*
E6-3	**0142: New Insights into the Saturation Behavior of the Hot Carrier Degradation in STI-based N-type LDMOS**
14:30 ~14:45	Zhuoqing Yu, Dan Gao, Yongsheng Sun, Junlin Huang *(Hisilicon, China)*
E6-4	**0081: The TID Response and HCI Degradation for multi-Vt nFinFETs**
14:45 ~15:00	Ruxue Yao, Hongliang Lu, Yuming Zhang, Yutao Zhang *(Xidian University, China)*

Friday, October 25, 13: 30 – 15: 15 Meeting Room 6
Session F6: Device Modeling II Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Xiaoyan Liu, Peking University, China

	Title
F6-1	**0059: Vertical Channel Transistor (VCT) for Advanced Logic and Memory Applications (invited)**
13:30 ~14:00	Mingmin Shi, Ran Bi, Ming Li *(Peking University, China; Beijing Advanced Innovation Center for Integrated Circuits, China)*
F6-2	**0423: High Precision I-V Characteristics SPICE Model for Silicon Carbide MOSFET**
14:00 ~14:15	Jinhong Shi, Yongxi Li, Jincheng Shi, Ruguan Li, Haimeng Huang, Hongqiang Yang *(University of Electronic Science and Technology of China, China; GRG Metrology & Test Group Co., Ltd., China)*
F6-3	**0440: An Analytical Model for Characterizing Density of States of Oxide Transistors**
14:15	Siyuan Hu, Chuanlin Sun, Junchen Dong, Zhensong Li, Kai Zhao, Dedong Han, Xing

~14:30	Zhang *(Peking University, China; Beijing Information Science & Technology University, China; Peking University Shenzhen Graduate School, China)*
F6-4	**0225: A Physics-Informed Neural Network Model for Body Potential Distribution in MOSFETs down to 50 K**
14:30 ~14:45	Honglin Wu, Fangxing Zhang, Xinyue Zhang, Baokang Peng, Wu Dai, Lining Zhang, Runsheng Wang, Ru Huang *(Peking University, China)*

Friday, October 25, 15: 30 – 17: 15

Friday, October 25, 15: 30 – 17: 15 Meeting Room 1
Session A5: FPGA Based Design Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Mingyu Wang, Sun Yat-sen University, China

	Title
A5-1	**0207: A Run-time Temperature Monitoring with Adaptive Duty Cycle Control for FPGA Applications**
15:30 ~15:45	Weizhi Li, Wangyong Chen, Haifeng Chen, Haoyu Zhang, Linlin Cai *(Sun Yat-sen University, China)*
A5-2	**0118: An FPGA-Based Top-K Gradient Compression Accelerator for Distributed Deep Learning Training**
15:45 ~16:00	Ziyao Wang, Jiayu Zhang, Kunyue Li, Jialei Sun, Feng Dong, Ke Chen, Yong Qiao, Jianfei Jiang *(National Key Laboratory of Advanced Micro and Nano Manufacture Technology, China; Beijing iQlYI Science & Technology Co.. Ltd.., China)*
A5-3	**0258: Dynamic-Matrix-Encryption Based Secure Strong PUF for Device Authentication Protocols**
16:00 ~16:15	Liangxiao Zhao, Gang Li, Pengjun Wang, Xuejiao Ma, Ziyu Zhou *(Wenzhou University, China; Wenzhou University of Technology, China; Ningbo University, China)*
A5-4	**0240: A Low Latency and High Throughout Hardware Design of Random Matrix Number Generator for FrodoKEM**
16:15 ~16:30	Shengfei Gu, Jiahao Lu, Tianze Huang, Jiaming Zhang, Kai Li, Cheng Wu, Mingbo Wang, Xianqi Mei, Ang Hu, Dongsheng Liu *(Huazhong University of Science and Technology, China; JinYinHu Laboratory, China)*
A5-5	**0465: A 4K60fps Ultra-Low-Latency Light Compression Encoder for Bandwidth-Constrained Scenarios**
16:30	Yanzhong Li, Leilei Huang, Yibo Fan *(Fudan University, China; East China Normal*

~16:45	*University, China)*
A5-6	**0086: Layer Pipelined Neural Network Accelerator Design on 2.5D FPGAs**
16:45 ~17:00	Mengxuan Wang, Chang Wu *(Fudan University, China)*
A5-7	**0237: Fast and Accurate Partial-Zoom Depth Estimation for SPAD LiDAR Readout on FPGA**
17:00 ~17:15	Lichen Feng, Hongwei Shan, Rundong Cai, Zhangming Zhu *(Xidian University, China)*

Friday, October 25, 15: 30 – 17: 15 Meeting Room 2
Session B7: Chip Test Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Shuo Li, Fudan University, China

	Title
B7-1	**0135: In Situ Localization Techniques of Defects in Advanced Semiconductor Devices from Macroscale to Atomistic-scale (invited)**
15:30 ~16:00	Jialu Huang, Jingming Zhou, Zuoyuan Dong, Runsheng Wang, Junhao Chu, Xing Wu *(East China Normal University, China; Peking University, China)*
B7-2	**0402: Wafer-Level Characterization of Ring-Oscillators Frequency Degradation in FinFET Technology**
16:00 ~16:15	Hao Chang, Dan Gao, Yongsheng Sun, Junlin Huang *(Hisilicon Technologies Co., LTD, China)*
B7-3	**0246: Exhaustive Application-Dependent Testing for FPGA Interconnect Resources**
16:15 ~16:30	Wenwei Chen, Xinyu He, Tongshu Ding, Jian Wang, Jinmei Lai *(Fudan University, China)*
B7-4	**0303: A Comprehensive and Efficient Instruction-level Testing Method for Processor**
16:30 ~16:45	Zixin Yang, Zhichao Wei, Huanlin Luo, Jian Wang, Jinmei Lai *(Fudan University, China; Shanghai Academy of Spaceflight Technology, China)*
B7-5	**0006: Thermal Effect and Calibration for High Precision On-Wafer Analog IC Probe Testing**
16:45 ~17:00	Daisuke Iimori, Takayuki Nakatani, Shogo Katayama, Gaku Ogihara, Shuhei Yamamoto, Misaki Takagi, Yujie Zhao, Jianglin Wei, Anna Kuwana, Keno Sato, Takashi Ishida, Toshiyuki Okamoto, Tamotsu Ichikawa, Kentaroh Katoh, Kazumi Hatayama, Haruo Kobayashi *(Gunma University, Japan; Shenyang University of*

	Chemical Technology, China; Yibin University, China; ROHM Semiconductor, Japan; Fukuoka University, Japan)

Friday, October 25, 15: 30 – 17: 15 Meeting Room 3
Session C7: Sensor & MEMS III Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Zhanfeng Huang, Sun Yat-sen University, China

	Title
C7-1	**0460: Waterproof and Wearable Power Sources (invited)**
15:30 ~16:00	Sixing Xiong; Kenjiro Fukuda, Takao Someya *(RIKEN, Japan; The University of Tokyo, Japan)*
C7-2	**0146: A CMOS Pixel Sensor for Precise Track and Charge Measurement of Cosmic Ray Nuclei**
16:00 ~16:15	Ruikai Zhang, Wen He, Shanqiang Yang, Min Luo, Chenxu Wang, Cunfeng Feng, Meng Wang, Liang Zhang, Anqing Wang, Jianing Dong, Dong Liu, Yan Niu, Yang Zhou, Yuehong Gong, Xiaoli Wang, Shucheng Shi *(Harbin Institute of Technology, China; Shandong University, China; Institute of High Energy Physics Chinese Academy of Sciences, China; ShanDong JiaoTong University, China)*
C7-3	**0429: $S_{c0.096}Al_{0.904}N$-Based Bimorph Piezoelectric Micro Machined Ultrasonic Transducers**
16:15 ~16:30	Ziye Zhai, Wenjuan Liu, Chengliang Sun *(Wuhan University, China)*

Friday, October 25, 15: 30 – 17: 15 Meeting Room 4
Session D7: 3D Integration Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Ming Li, Peking University, China

	Title
D7-1	**0283: HISIM: Design Exploration of 2.5D/3D Heterogeneous Integration for AI Computing (Invited)**
15:30 ~16:00	Zhenyu Wang, Pragnya Sudershan Nalla, Jingbo Sun, A. Alper Goksoy, Sumit K. Mandal, Jae-sun Seo, Vidya A. Chhabria, Jeff Zhang, Chaitali Chakrabarti, Umit Y. Ogras, Yu Cao *(Arizona State University, USA; University of Minnesota, USA; University of Wisconsin-Madison,USA; Indian Institute of Science, India; Cornell Tech, USA)*
D7-2	**Analysis of Current Status and Trends in Microsystem Integration Technology Based on TSV Advanced Packaging (Invited)**
16:00	Hua Yao *(Natural-Integration Advanced Semiconductor Technology Co.，Ltd., China)*

~16:30	
D7-3	**0431: Flip 3D (F3D): A Novel 3D Integration Technology with Dual-side Integration Capabilities (invited)**
16:30 ~17:00	Heng Wu, Haoran Lu, Runsheng Wang, Ming Li, Yibo Lin, Weihai Bu, Jin Kang, Ru Huang *(Peking University, China)*
D7-4	**0035: Modeling and Simulation of A Conical 3D Monopole Antenna Embedded in Substrate for WNoC**
17:00 ~17:15	Junhao Wang, Ziyu Liu, Zhiyuan Zhu, Lin Chen, Qingqing Sun, Wei David Zhang *(Southwest University, China; Fudan University, China; Jiashan Fudan Institute, China)*

Friday, October 25, 15: 30 – 17: 15 Meeting Room 5
Session E7: Reliability III Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Zhigang Ji, Shanghai Jiaotong University, China

	Title
E7-1	**0463: Modeling and Parameter Extraction of Semiconductor Devices for Simulation and Design Optimization of ESD Protection Circuits on BCD Technologies for Automobile and Industry Applications (invited)**
15:30 ~16:00	Yuhua Cheng, Wei-wei Yu, Eugene Worley *(Peking University, China; Silicon Crossing, LLC, USA)*
E7-2	**0224: A Novel Double-zener Process and Multiplex Design for High-power Surge and High-speed ESD Devices Development**
16:00 ~16:15	Zhao Qi, YiRui Jia, Hongquan Chen, Ming Qiao, Zhaoji Li, Bo Zhang *(University of Electronic Science and Technology of China (UESTC), China;)*
E7-3	**0206: The Non-monotonic Instability of V_{TH} and $R_{ds,on}$ in P-GaN Gate HEMTs Under Repetitive Short Circuit Stress: The Role of Electric-field & Selfheating Effect**
16:15 ~16:30	Long Wang, Ning Yang, Shuting Huang, Jianggen Zhu, Kuangli Chen, Chao Feng, Haolin Hu, Wei Zeng, David Zhou, Yuxi Wan, Bo Zhang, and Qi Zhou *(University of Electronic Science and Technology of China (UESTC), China; Shenzhen Pinghu Laboratory,China)*

Friday, October 25, 15: 30 – 17: 15 Meeting Room 6
Session F7: Device Modeling III Sheraton Zhuhai Hotel 2nd Floor
Session Chair: Prof. Chunlei Wu, Fudan University, China

	Title
F7-1	**0448: Neural Network Assisted Mosfets Model Development (invited)**
15:30 ~16:00	Xiaoyan Liu *(Peking University, China)*
F7-2	**0421:Modeling the Transient Characteristics with Trap Behaviors in LTPS-TFTs**
16:00 ~16:15	Haolin Li, Zheng Zhou, Xiaoyan Liu *(Peking University, China; Beijing Advanced Innovation Center for Integrated Circuits, China)*
F7-3	**0277: A Novel β-Ga_2O_3-Based Enhancement-Mode Transistor Combining Heterojunction Gate and Fin shaped Gate**
16:15 ~16:30	Yu Shao, Yunlong He, Xiaoli Lu, Songyao Wang, Xuefeng Zheng, Xiaohua Ma, Yue Hao *(Xidian University, China)*
F7-4	**0358: Electrical Characteristics and Thermal Reliability Investigation of TreeFET, FishboneFET, CombLikeFET and NSFET**
16:30 ~16:45	Mingyu Ma, Wenbin Wang, Haokun Li, Shujun Gao, Hailong You, Cong Li *(Xidian University, China)*

Author Index

A5-2	Ke	Chen
E2-4	Kuangli	Chen
E7-3	Kuangli	Chen
P1-52	Lei	Chen
D7-4	Lin	Chen
B2-4	Liying	Chen
P1-40	Long	Chen
P2-8	Ning	Chen
P2-38	Peng	Chen
A3-1	Qi	Chen
C1-4	Quan	Chen
D5-1	Rong	Chen
P1-58	Ruolan	Chen
D5-2	Shiyou	Chen
P1-58	Shulan	Chen
P1-21	Shushi	Chen
C2-4	Sichao	Chen
A1-5	Song	Chen
P1-26	Tao	Chen
A5-1	Wangyong	Chen
F5-4	Wangyong	Chen
P2-22	Wangyong	Chen
E2-3	Wanjun	Chen
E3-3	Wanjun	Chen
P2-39	Wanjun	Chen
P2-47	Wanjun	Chen
P2-15	Weifeng	Chen
P2-25	Wenjun	Chen
B7-3	Wenwei	Chen
P1-25	Xiang	Chen
B2-2	Xiaowen	Chen
P1-44	Xibo	Chen
P2-47	Xinghuan	Chen
F1-5	Xingyu	Chen
P2-48	Xinyang	Chen
D5-3	Xu	Chen
B3-4	Xurui	Chen
P2-58	Yanning	Chen
C3-4	Yifei	Chen
P1-62	Yifei	Chen
B3-3	Yong	Chen
P1-25	Yongliang	Chen
P1-35	Yu	Chen
P1-4	Yuhua	Chen
A2-6	Yujie	Chen
P2-53	Yuwei	Chen
P1-67	Yuyang	Chen
B4-3	Zhenghan	Chen
A2-4	Zhikang	Chen
B4-3	Zhongjian	Chen
A3-1	Zirui	Chen
A4-4	Zixiang	Chen
P2-30	Zixun	Chen
D1-5	Hsing-Mao	Cheng
P2-32	Junji	Cheng
P2-44	Junji	Cheng
B3-3	Kai	Cheng
P2-41	Lin	Cheng
P2-49	Lin	Cheng
C5-2	Nannan	Cheng
P1-45	Yu	Cheng
E7-1	Yuhua	Cheng
D7-1	Vidya A.	Chhabria
P2-19	Chun Yee	Chu
B7-1	Junhao	Chu
P1-61	Yu	Chu
A3-4	Yuan-Yu	Chuang
T1-1	Cor	Claeys
C1-1	Jose G. F.	Coutinho
E3-2	Miao	Cui
E1-1	Wentao	Cui
A1-4	Xianghe	Cui
P1-25	Xiaole	Cui
P1-44	Xuecheng	Cui
A2-1	Yijun	Cui
SS-4	Wenliang	Da
B2-5	Chengxing	Dai
P1-39	Chengxing	Dai
E6-1	F.	Dai
P2-44	Ming	Dai
F1-4	Saifei	Dai
F2-6	Saifei	Dai
F4-5	Saifei	Dai
P2-24	Shun-Qi	DAI
P1-34	Siyao	Dai
F6-4	Wu	Dai
C2-4	Yuan	Dai
P2-6	Xie	Dan

E2-1	Chenkai	Deng
E1-3	Gaoqiang	Deng
P2-35	Haonan	Deng
E4-1	Hongfei	Deng
C3-3	Jie	Deng
B3-6	Lei	Deng
P1-49	Lei	Deng
P1-51	Lei	Deng
P1-53	Lei	Deng
E2-3	Shiyu	Deng
E2-5	Xiaochuan	Deng
P2-58	Yongfeng	Deng
C6-1	Yann	Deval
D1-3	Boyuan	Di
P2-15	Guojian	Ding
E2-2	Qi	Ding
P2-12	Rongzheng	Ding
A2-6	Runye	Ding
P2-26	Shijin	Ding
B7-3	Tongshu	Ding
E6-1	X.	Ding
F2-6	Yajing	Ding
D1-4	Yingtao	Ding
F1-5	Yong	Ding
P1-30	Yong	Ding
P1-44	Yong	Ding
B3-6	Yuanhong	Ding
P1-49	Yuanhong	Ding
A3-5	Chenge	Dong
A5-2	Feng	Dong
P2-27	Haitao	Dong
C7-2	Jianing	Dong
C5-4	Junchen	Dong
F6-3	Junchen	Dong
P1-17	Lei	Dong
P2-45	Lihang	Dong
P2-46	Lihang	Dong
P1-66	Qingyang	Dong
P1-32	Qiyun	Dong
C3-2	Shijiao	Dong
P2-7	Shuangqi	Dong
E4-2	Xiaoping	Dong
D5-3	Yaoqi	Dong
B7-1	Zuoyuan	Dong

SS-5	Chunmeng	Dou
E3-4	Fengyu	Du
P2-34	Fengyu	Du
C5-4	Gang	Du
F2-2	Peiyuan	Du
P2-57	Rongxin	Du
P2-14	Wencai	Du
P1-26	Yiran	Du
E2-4	Enchuan	Duan
E2-1	Difei	Fan
F4-5	Wenbo	Fan
P2-31	Xiangrui	Fan
A5-5	Yibo	Fan
P1-21	Yibo	Fan
P1-27	Yibo	Fan
P1-28	Yibo	Fan
B3-4	Jian	Fang
P1-30	Rui	Fang
P2-43	Baoyan	Feng
E7-3	Chao	Feng
C7-2	Cunfeng	Feng
P2-56	Jiekai	Feng
A5-7	Lichen	Feng
B5-2	Lichen	Feng
P2-15	Qi	Feng
F1-2	Yang	Feng
P2-27	Yang	Feng
E4-2	Chengwen	Fu
C1-3	Fangfa	Fu
P2-20	Fangfa	Fu
E1-4	Hao	Fu
P2-31	Hao	Fu
P2-33	Hao	Fu
P2-53	Jing	Fu
B1-3	Peiyuan	Fu
P2-32	Xiaojun	Fu
B1-1	Yonggang	Fu
C7-1	Kenjiro	Fukuda
B2-6	Weixin	Gai
P1-3	Jie	Gan
B7-2	Dan	Gao
E6-3	Dan	Gao
P2-58	Dawei	Gao
T2-3	Hao	Gao

P1-67	Jianjun	Gao
P1-2	Liang	Gao
E6-2	Rui	Gao
C4-4	Ruiyi	Gao
F7-4	Shujun	Gao
C1-3	Xingyu	Gao
P2-20	Xingyu	Gao
C2-4	Xuchen	Gao
P2-52	Hao	Ge
C3-1	Wansi	Ge
C2-1	Ziang	Ge
D7-1	A. Alper	Goksoy
P2-26	Dmitriy Anatolyevich	Golosov
E4-1	Sen	Gong
A2-5	Yi	Gong
F2-4	Yong	Gong
C7-2	Yuehong	Gong
B1-2	Fei	Gou
P2-26	Chenjie	Gu
D4-5	Hanzhi	Gu
F5-1	Hanzhi	Gu
F5-3	Hanzhi	Gu
A5-4	Shengfei	Gu
P1-9	Wenxian	Gu
E4-3	Yong	Gu
D1-2	Chaowen	Guan
D1-1	Qingzhong	Gui
P2-53	Anran	Guo
A3-5	Huimeng	Guo
A4-5	Jiajie	Guo
P2-61	Jianping	Guo
P1-51	Jiyuan	Guo
P2-64	Junxiong	Guo
B5-1	Mingqiang	Guo
E1-5	Pei	Guo
P2-28	Peng	Guo
A1-6	Qingyu	Guo
P1-30	Shuaicheng	Guo
P1-16	Tianyu	Guo
F1-2	Xinyi	Guo
P2-28	Xinyi	Guo
B2-2	Yang	Guo
P2-3	Yirong	Guo
A3-2	Yiting	Guo
B6-3	Yuekang	Guo
P2-64	Yufeng	Guo
P2-1	Yulin	Guo
E2-2	Yutao	Guo
D1-1	Yuzheng	Guo
F2-1	Zhijian	Guo
B6-2	Zhiwang	Guo
C2-2	Zizheng	Guo
P2-34	Chao	Han
P2-40	Chao	Han
P2-41	Chao	Han
P2-43	Chao	Han
F6-3	Dedong	Han
P2-60	Dongxiang	Han
F2-2	Genquan	Han
P1-44	Genquan	Han
P2-11	Hongyan	Han
A1-2	Jun	Han
A2-2	Jun	Han
A4-1	Jun	Han
B6-2	Jun	Han
P1-33	Jun	Han
P1-34	Jun	Han
P1-36	Jun	Han
F2-6	Kai	Han
F4-5	Kai	Han
P1-65	Kefeng	Han
P1-2	Lixiang	Han
P1-52	Rong	Han
F2-6	Runhao	Han
F4-5	Runhao	Han
D2-5	Wei-Hua	Han
P2-11	Jianfeng	Hang
E1-5	Linyao	Hao
F2-2	Yue	Hao
F7-3	Yue	Hao
P2-36	Yue	Hao
P1-28	Zhijian	Hao
B7-5	Kazumi	Hatayama
B1-1	Anping	He
P1-27	Chenlong	He
P1-21	Huanxiang	He
E2-2	Kunqin	He

P1-13	Lin	He
E5-3	Nailong	He
P2-22	Songxuan	He
P1-60	Weitao	He
C7-2	Wen	He
B7-3	Xinyu	He
D5-3	Yanping	He
P1-35	Yingtong	He
P1-46	Yu	He
A3-1	Yuhui	He
F7-3	Yunlong	He
D4-4	Toshiro	Hiramoto
P1-30	Bo	Hong
P2-36	Bin	Hou
E4-3	Xiangyu	Hou
A5-4	Ang	Hu
P1-14	Ang	Hu
P1-54	Ang	Hu
P1-59	Ang	Hu
P1-64	Ang	Hu
E7-3	Haolin	Hu
E2-5	Jiahao	Hu
C1-4	Jianguo	Hu
D1-4	Jianzhi	Hu
P2-56	Jiaxuan	Hu
P1-55	Jing	Hu
A1-4	Jingyi	Hu
P2-48	Qiang	Hu
P1-57	Shuang	Hu
F6-3	Siyuan	Hu
F2-6	Tao	Hu
F4-5	Tao	Hu
B3-5	Weiming	Hu
P1-20	Yi	Hu
P1-42	Yibo	Hu
P2-52	Yibo	Hu
P2-9	Yibo	Hu
P2-63	Yidi	Hu
P1-46	Yuhan	Hu
D4-3	Yushen	Hu
F3-2	Zeyu	Hu
C3-1	Younan	Hua
D1-5	Guang-Wei	Huang
F6-2	Haimeng	Huang
P2-29	Haimeng	Huang
P2-32	Haimeng	Huang
P2-37	Haimeng	Huang
P2-44	Haimeng	Huang
P2-16	Heyi	Huang
B7-1	Jialu	Huang
F2-7	Jian	Huang
P2-39	Jie	Huang
E2-2	Jun	Huang
B7-2	Junlin	Huang
E6-3	Junlin	Huang
P2-33	Lei	Huang
A5-5	Leilei	Huang
P1-21	Leilei	Huang
P1-24	Leilei	Huang
P1-27	Leilei	Huang
P1-28	Leilei	Huang
D5-2	Menglin	Huang
E4-2	Mingmin	Huang
P2-11	Qianqian	Huang
P2-57	Qihang	Huang
C2-2	Ru	Huang
D6-3	Ru	Huang
D7-3	Ru	Huang
E5-1	Ru	Huang
F4-3	Ru	Huang
F4-4	Ru	Huang
F6-4	Ru	Huang
P2-11	Ru	Huang
E4-3	Runhua	Huang
E2-4	Shuting	Huang
E7-3	Shuting	Huang
P2-57	Shuting	Huang
P2-35	Shuyang	Huang
A5-4	Tianze	Huang
F5-2	Tingrui	Huang
P2-64	Wen	Huang
E4-2	Wende	Huang
P2-18	Yifeng	Huang
A1-3	Yujie	Huang
P1-23	Yujie	Huang
P1-10	Zepeng	Huang
P1-10	Zhaofeng	Huang
P2-16	Jiali	Huo

E2-2	Qiuyue	Huo
B7-5	Tamotsu	Ichikawa
C6-1	Ayoub Ait	Ihda
B7-5	Daisuke	Iimori
E6-1	K.	Imura
SS-7	Michiko	Inoue
B7-5	Takashi	Ishida
P1-18	Xiaoli	Ji
P2-62	Xiaoli	Ji
D6-2	Zhigang	Ji
E6-2	Zhigang	Ji
P1-60	Bin	Jia
P1-22	Guowen	Jia
P2-15	Haiqiang	Jia
B3-6	Longjiang	Jia
P1-49	Longjiang	Jia
P2-45	Pengfei	Jia
P2-46	Pengfei	Jia
P2-11	Rundong	Jia
P2-37	Yiming	Jia
E5-3	Yirui	Jia
E7-2	Yirui	Jia
F1-1	Anquan	Jiang
C6-2	Chen	Jiang
P2-14	Chunsheng	Jiang
B5-1	Dongyang	Jiang
A5-2	Jianfei	Jiang
P1-5	Li	Jiang
P1-44	Nan	Jiang
P2-10	Renjie	Jiang
P2-16	Renjie	Jiang
P2-17	Renjie	Jiang
P1-23	Tong	Jiang
P1-66	Xin	Jiang
P2-44	Xingli	Jiang
P2-29	Yunteng	Jiang
E3-2	Zijin	Jiang
F2-1	Chengji	Jin
F2-2	Chengji	Jin
B6-3	Jing	Jin
P2-54	Xiangliang	Jin
C6-4	Xin	Jin
P1-26	Yu	Jin
B3-4	Yuhan	Jin
P2-29	Zhenghao	Jin
A1-3	Minge	Jing
P1-23	Minge	Jing
P2-37	Zhiwei	Jing
P2-34	Haobo	Kang
D7-3	Jin	Kang
F2-3	Jinfeng	Kang
A3-6	Wang	Kang
A1-5	Yi	Kang
B7-5	Shogo	Katayama
B4-1	Kentaroh	Katoh
B7-5	Kentaroh	Katoh
F1-3	Xiaoyu	Ke
P1-48	Xuanxin	Ke
P1-11	Ikhwan	Kim
SS-1	Tony Tae-Hyoung	Kim
B4-1	Haruo	Kobayashi
B7-5	Haruo	Kobayashi
D4-4	Masaharu	Kobayashi
F2-1	Masaharu	Kobayashi
B4-1	Yutaro	Kobayashi
E4-1	Moufu	Kong
P2-44	Moufu	Kong
P2-48	Moufu	Kong
F3-1	Anuj	Kumar
D2-3	Chia-Yo	Kuo
B7-5	Anna	Kuwana
B7-3	Jinmei	Lai
B7-4	Jinmei	Lai
E3-2	Sang	Lam
C3-4	Haokun	Lan
P2-35	Zhengyu	Lang
C6-1	Herve	Lapuyade
D4-3	Tengteng	Lei
D1-3	Wenyu	Lei
P2-24	Yuan	LEI
P1-53	Angyang	Li
B2-4	Bangtian	Li
B2-3	Biao	Li
P1-51	Bin	Li
B3-1	Bo	Li
P2-56	Chengxin	Li
F7-4	Cong	Li

P2-51	Cong	Li
P1-58	Congrui	Li
C6-4	Dejian	Li
B4-5	Dengquan	Li
P1-47	Dengquan	Li
F2-2	Dongya	Li
A2-3	Gang	Li
A5-3	Gang	Li
P2-53	Guowei	Li
P1-14	Hao	Li
P1-2	Hao	Li
F7-4	Haokun	Li
F7-2	Haolin	Li
B2-3	Hong	Li
A2-3	Hui	Li
A2-1	Jiang	Li
F3-3	Jianguo	Li
P1-52	Jianwen	Li
B3-5	Jianzheng	Li
P1-8	Jianzheng	Li
P2-3	Jie	Li
P1-68	Jincheng	Li
P2-42	Jing	Li
F4-4	Jinshan	Li
P1-43	Jinyu	Li
P2-10	Junfeng	Li
A5-4	Kai	Li
P1-14	Kaiyue	Li
F4-5	Kanyi	Li
A5-2	Kunyue	Li
C1-3	Lei	Li
P2-20	Lei	Li
P2-10	Lianlian	Li
P2-38	Luping	Li
P2-46	Luping	Li
A1-6	Meng	Li
P1-36	Menghan	Li
D7-3	Ming	Li
F6-1	Ming	Li
P2-38	Ming	Li
D1-4	Mingjie	Li
P2-7	Mingjie	Li
B1-5	Ningning	Li
P1-53	Ningning	Li
P1-7	Ningning	Li
P2-56	Peng	Li
P2-10	Qingkun	Li
E2-2	Renxiong	Li
P1-54	Renxuan	Li
P1-64	Renxuan	Li
E1-1	Rophina	Li
F6-2	Ruguan	Li
P2-29	Ruguan	Li
P2-53	Ruizhi	Li
P1-1	Ruoyu	Li
F2-3	Shangze	Li
E1-2	Sheng	Li
P2-36	Shiming	Li
C3-1	Shuai	Li
P2-6	Shuaihong	Li
E4-1	Shurui	Li
P2-1	Siteng	Li
P1-66	Tianyu	Li
D5-4	Tiaoyang	Li
P2-5	Tiaoyang	Li
D5-1	Wang	Li
P2-27	Wanyu	Li
B4-4	Wei	Li
C3-2	Wei	Li
C6-5	Wei	Li
P1-26	Wei	Li
P1-40	Wei	Li
P2-38	Wei	Li
P2-45	Wei	Li
P2-46	Wei	Li
A5-1	Weizhi	Li
A1-3	Wenhong	Li
P1-23	Wenhong	Li
P2-61	Wenhong	Li
D2-2	Wenwu	Li
E1-5	Xiangnan	Li
C3-1	Xiaomin	Li
B2-5	Xin	Li
P1-39	Xin	Li
F5-4	Xingyu	Li
E2-5	Xuan	Li
B2-4	Xueke	Li
F2-5	Yang	Li

P2-16	Yanqing	Li
A5-5	Yanzhong	Li
E2-2	Yaxin	Li
C5-2	Yichen	Li
C5-2	Yongjia	Li
F6-2	Yongxi	Li
F3-4	Yongxiang	Li
C1-2	Yuan	Li
P2-13	Yudong	Li
A3-5	Yujia	Li
C5-1	Yutao	Li
F2-1	Yutao	Li
P2-38	Zehong	Li
P2-45	Zehong	Li
P2-46	Zehong	Li
E7-2	Zhaoji	Li
A1-4	Zhenmin	Li
F6-3	Zhensong	Li
P2-5	Zhenwei	Li
P1-5	Zhihuai	Li
C2-5	Zhiqiang	Li
C6-6	Zhiqun	Li
P1-55	Zhiwei	Li
P2-29	Zonglin	Li
F1-5	Xueguang	Lian
P1-44	Xueguang	Lian
P2-50	Jie	Liang
E1-1	Jingyuan	Liang
P2-25	Libin	Liang
F4-3	Ling	Liang
F4-4	Ling	Liang
F1-3	Min	Liao
P2-56	Pengfei	Liao
B1-3	Xufeng	Liao
C6-4	Xufeng	Liao
P1-18	Yiming	Liao
P2-62	Yiming	Liao
A4-5	Guangyao	Lin
B2-3	Jinpeng	Lin
A3-4	Jyi-Tsong	Lin
D1-5	Jyi-Tsong	Lin
D2-3	Jyi-Tsong	Lin
D3-4	Jyi-Tsong	Lin
D4-2	Jyi-Tsong	Lin
P2-64	Lin	Lin
C2-2	Yibo	Lin
D7-3	Yibo	Lin
A1-2	Zipeng	Ling
C3-1	Juin J.	Liou
E4-3	Ao	Liu
D5-3	Chang	Liu
E2-3	Chao	Liu
P2-39	Chao	Liu
C4-4	Chen	Liu
C7-2	Dong	Liu
C2-5	Donglin	Liu
A5-4	Dongsheng	Liu
P1-14	Dongsheng	Liu
P1-2	Dongsheng	Liu
P1-54	Dongsheng	Liu
P1-59	Dongsheng	Liu
P1-64	Dongsheng	Liu
P2-58	Fang	Liu
B1-4	Fei	Liu
P2-15	Feng	Liu
P1-50	Haitao	Liu
B2-2	Heyuan	Liu
P1-16	Houfang	Liu
B3-4	Huajie	Liu
F2-2	Huan	Liu
P1-18	Jie	Liu
P1-15	Jin	Liu
D5-3	Jinbiao	Liu
F1-2	Jing	Liu
A4-3	Jingjing	Liu
B6-1	Jingjing	Liu
C5-3	Jingjing	Liu
P1-12	Jingjing	Liu
P1-38	Jingjing	Liu
P1-48	Jingjing	Liu
B2-2	Jingtian	Liu
A3-6	Junzhan	Liu
P1-24	Kai	Liu
E3-4	Keyu	Liu
P2-34	Keyu	Liu
B1-3	Lianxi	Liu
C6-4	Lianxi	Liu
B5-3	Linghao	Liu

C4-2	Liyuan	Liu
B5-4	Maliang	Liu
C3-4	Maliang	Liu
P1-15	Maliang	Liu
P1-62	Maliang	Liu
D5-3	Mingshan	Liu
P1-4	Qianhui	Liu
E1-5	Renkuan	Liu
F4-1	Robert	Liu
P1-17	Rui	Liu
P1-50	Rui	Liu
P1-59	Shengxiang	Liu
P2-16	Shuang	Liu
P2-17	Shuang	Liu
P1-47	Shubin	Liu
E4-3	Siayang	Liu
P2-6	Sicheng	Liu
C5-2	Siyang	Liu
E1-2	Siyang	Liu
E1-4	Siyang	Liu
P2-31	Siyang	Liu
P2-33	Siyang	Liu
B1-2	Wei	Liu
E3-3	Wei	Liu
E5-2	Wei	Liu
F3-3	Wei	Liu
D5-3	Weibing	Liu
A2-1	Weiqiang	Liu
C7-3	Wenjuan	Liu
P2-26	Wenjun	Liu
P2-62	Xiangze	Liu
F2-7	Xianping	Liu
A3-3	Xiaotao	Liu
F2-3	Xiaoyan	Liu
F7-1	Xiaoyan	Liu
F7-2	Xiaoyan	Liu
P1-63	Xiaoyan	Liu
P2-59	Xiaoyan	Liu
E1-3	Xindi	Liu
F2-2	Yan	Liu
E3-4	Yancong	Liu
P2-30	Yang	Liu
D5-4	Yanheng	Liu
P1-26	Yanjiang	Liu
A2-6	Yao	Liu
P2-26	Yinchi	Liu
P2-64	Yu	Liu
P1-46	Yuchen	Liu
C3-1	Yuqing	Liu
A2-6	Yuyang	Liu
D5-3	Zhe	Liu
P2-1	Zhifang	Liu
P2-6	Zhifang	Liu
P1-61	Zhiqing	Liu
P2-29	Zhiwan	Liu
P2-7	Zhongyang	Liu
D4-4	Zihao	Liu
D7-4	Ziyu	Liu
E3-2	Ziyu	Liu
D3-3	Fucai	Liu
P2-37	Jieyu	Long
C1-3	Keli	Long
P2-20	Keli	Long
A3-3	Bin	Lu
F2-4	Cimang	Lu
A3-3	Haoran	Lu
D7-3	Haoran	Lu
E6-4	Hongliang	Lu
P2-26	Hongliang	Lu
P2-49	Hongliang	Lu
A5-4	Jiahao	Lu
B2-2	Jianzhuang	Lu
E1-3	Jinlong	Lu
P1-32	Sheng	Lu
P1-36	Sheng	Lu
P2-42	Weihao	Lu
B4-3	Wengao	Lu
F7-3	Xiaoli	Lu
P2-10	Yupeng	Lu
P2-16	Yupeng	Lu
P2-17	Yupeng	Lu
C3-1	Zhenghao	Lu
C2-4	Wai-Shing	Luk
C1-1	Wayne	Luk
B7-4	Huanlin	Luo
P2-17	Jie	Luo
D5-3	Jun	Luo
C7-2	Min	Luo

P2-56	Ping	Luo
P2-1	Weijia	Luo
P1-66	Weijun	Luo
E1-3	Xiaorong	Luo
E1-5	Xiaorong	Luo
E3-3	Xixi	Luo
F1-5	Hankun	Lv
P1-45	Qi	Lv
A2-1	Fei	Lyu
D6-4	Haiyuan	Lyu
P2-55	Hongliang	Lyu
P2-48	Lewei	Lyu
P1-10	Liangjian	Lyu
P1-9	Liangjian	Lyu
B4-4	Bingbing	Ma
P2-53	Huaping	Ma
D4-5	Jian	Ma
F5-1	Jian	Ma
F5-3	Jian	Ma
P2-15	Jiang	Ma
P2-51	Jianghao	Ma
C5-2	Jie	Ma
E4-3	Jie	Ma
C3-1	Kaixue	Ma
P2-57	Luanxi	Ma
F7-4	Mingyu	Ma
P2-51	Mingyu	Ma
P1-56	Shunli	Ma
P1-37	Wenzhe	Ma
F7-3	Xiaohua	Ma
P2-36	Xiaohua	Ma
C3-2	Xingyu	Ma
P2-30	Xudong	Ma
A5-3	Xuejiao	Ma
E1-2	Yanfeng	Ma
E4-2	Yao	Ma
F2-1	Yuanxiao	Ma
T2-1	Yufei	Ma
C3-1	Nagarajan	Mahalingam
F3-1	Souvik	Mahapatra
D7-1	Sumit K.	Mandal
C1-5	Yiqing	Mao
C2-4	Yiqing	Mao
B1-2	Zhifeng	Mao

B3-3	Pui-In	Mark
B3-3	Rui P.	Martins
B2-4	Jian	Mei
B3-6	Jian	Mei
P1-49	Jian	Mei
P1-51	Jian	Mei
P1-53	Jian	Mei
A5-4	Xianqi	Mei
P2-48	Fanxin	Meng
C3-1	Fanyi	Meng
A3-6	Jinyao	Mi
A3-1	Xiangshui	Miao
P1-60	Hao	Min
P1-35	Tai	Min
SS-6	Kyeong-Sik	Min
D4-4	Tomoko	Mizutani
F2-1	Fei	Mo
A4-3	Wenji	Mo
B6-1	Wenji	Mo
C5-3	Wenji	Mo
D4-4	Takahiro	Mori
K3-1	Boris	Murmann
B7-5	Takayuki	Nakatani
D7-1	Pragnya Sudershan	Nalla
P1-26	Longmei	Nan
E1-1	Wai Tung	Ng
P2-19	Wai Tung	Ng
P1-66	Xiangran	Ni
P2-57	Yanyu	Nie
E2-2	Ning	Ning
P1-46	Ning	Ning
E6-1	G.	Niu
B2-3	Ruiting	Niu
C7-2	Yan	Niu
B4-1	Tomohiko	Ogawa
B7-5	Gaku	Ogihara
D7-1	Umit Y.	Ogras
D4-4	Hiroshi	Oka
B7-5	Toshiyuki	Okamoto
E3-1	Mikael	Östling
C1-4	Lingyun	Ouyang
B5-1	Rui	P. Martins
E1-2	Chuanqi	Pan

P1-20	Jie	Pan
P1-30	Yu	Pan
D3-2	Sebastian	Pazos
F6-4	Baokang	Peng
P2-42	Chaoyang	Peng
B2-5	Chunyu	Peng
P1-39	Chunyu	Peng
F1-5	Daixiao	Peng
P1-44	Daixiao	Peng
P2-15	Lijuan	Peng
A1-3	Liyuan	Peng
P1-23	Liyuan	Peng
E2-2	Lulu	Peng
B5-4	Luo	Peng
E2-2	Pan	Peng
F1-5	Sirui	Peng
E1-5	Xiaosong	Peng
B4-2	Xizhu	Peng
F2-7	Yungen	Peng
P2-4	Zhirong	Peng
K1-1	Paul	Penzes
P1-52	Jie	Pu
C3-3	Gengzhen	Qi
C6-3	Gengzhen	Qi
P2-49	Junjun	Qi
E5-3	Zhao	Qi
E7-2	Zhao	Qi
A3-3	Hua	Qiang
P2-49	Jiantao	Qiao
C1-3	Liangquan	Qiao
P2-20	Liangquan	Qiao
E2-2	Ming	Qiao
E5-3	Ming	Qiao
E7-2	Ming	Qiao
C2-5	Shushan	Qiao
A5-2	Yong	Qiao
P2-44	Yuan	Qiao
P2-38	Haifeng	Qin
A3-6	Haiyan	Qin
B3-5	Yajie	Qin
P1-11	Yajie	Qin
P1-8	Yajie	Qin
P1-19	Zhigang	Qin
B3-1	Hao	Qiu
F2-4	Xiang	Qiu
D6-5	XinPing	Qu
P1-45	Yang	Qu
E5-2	Yiming	Qu
F3-3	Yiming	Qu
P1-69	Yiming	Qu
C1-5	Yuwei	Qu
C1-1	Zhiqiang	Que
P1-41	Amy	Rao
P2-38	Qiansheng	Rao
B5-3	Junyan	Ren
B5-5	Junyan	Ren
P2-38	Min	Ren
A2-4	Mingze	Ren
P1-57	Mingze	Ren
D6-2	Pengpeng	Ren
C5-1	Tianling	Ren
P1-16	Tianling	Ren
P2-13	Tianling	Ren
P2-60	Tianling	Ren
A3-5	Tingrui	Ren
P2-11	Ye	Ren
P1-6	Yuan	Ren
P1-42	Zhipeng	Ren
P2-52	Zhipeng	Ren
P2-9	Zhipeng	Ren
C6-1	Francois	Rivet
D1-1	John	Robertson
C6-1	Stephane	Rochette
K2-2	Fred	Roozeboom
P2-10	Guanqiao	Sang
B2-2	Hao	Sang
P2-28	Pengpeng	Sang
D4-4	Takuya	Saraya
B7-5	Keno	Sato
C4-3	Mohamad	Sawan
D7-1	Jae-sun	Seo
C1-4	Yanliang	Sha
A5-7	Hongwei	Shan
B5-2	Hongwei	Shan
F4-4	Linbo	Shan
P1-54	Xiaoyu	Shan
P1-64	Xiaoyu	Shan
D2-5	Zi-Meng	Shang

D5-1	Sicong	Shao
F1-3	Xianzhou	Shao
F1-4	Xianzhou	Shao
F2-6	Xianzhou	Shao
F4-5	Xianzhou	Shao
F7-3	Yu	Shao
D6-5	YunHao	Shao
P1-40	Shaohao	Shao Hao
D4-5	Boqian	Shen
F5-1	Boqian	Shen
F5-3	Boqian	Shen
D1-2	Chao	Shen
P2-63	Chao	Shen
C5-1	Haoyuan	Shen
P1-20	Hongwei	Shen
P2-8	Lei	Shen
P1-45	Rensheng	Shen
P2-23	Rensheng	Shen
C6-2	Xiaohan	Shen
F5-2	Xinxin	Shen
B1-2	Bin	Sheng
P1-24	Chunqi	Shi
F6-2	Jincheng	Shi
F6-2	Jinhong	Shi
F6-1	Mingmin	Shi
C7-2	Shucheng	Shi
P2-62	Tiantian	Shi
P1-33	Xinhua	Shi
B3-1	Yi	Shi
E3-3	Yu	Shi
P2-47	Yu	Shi
P2-57	Yu	Shi
P1-19	Yue	Shi
B5-1	Sai-Weng	Sin
C7-1	Takao	Someya
E4-3	Bai	Song
P2-1	Jiafeng	Song
P2-34	Qingwen	Song
P2-40	Qingwen	Song
P2-41	Qingwen	Song
P2-43	Qingwen	Song
E3-4	Qinwen	Song
C4-1	Ruiyang	Song
E1-3	Wei	Song
E1-5	Wei	Song
F2-3	Xujin	Song
P1-9	Yifan	Song
A1-4	Yukun	Song
E1-4	Zhaosu	Song
P1-12	Xinying	Su
P1-14	Yanwen	Su
C7-3	Chengliang	Sun
F6-3	Chuanlin	Sun
F2-3	Dijiang	Sun
A4-3	Haoning	Sun
B6-1	Haoning	Sun
C5-3	Haoning	Sun
P1-48	Haoning	Sun
A1-1	Hongbin	Sun
A5-2	Jialei	Sun
P2-31	Jiameng	Sun
D7-1	Jingbo	Sun
B3-1	Junyi	Sun
B6-1	Kangkang	Sun
P1-48	Kangkang	Sun
P1-59	Ke	Sun
D1-2	Leihao	Sun
D4-5	Qingqing	Sun
D7-4	Qingqing	Sun
F5-1	Qingqing	Sun
F5-3	Qingqing	Sun
P1-17	Quan	Sun
P1-50	Quan	Sun
E2-3	Ruize	Sun
E3-3	Ruize	Sun
P2-39	Ruize	Sun
P2-47	Ruize	Sun
C5-2	Weifeng	Sun
E1-2	Weifeng	Sun
E1-4	Weifeng	Sun
E4-3	Weifeng	Sun
F5-2	Weifeng	Sun
P2-31	Weifeng	Sun
P2-33	Weifeng	Sun
A1-5	Wendi	Sun
C5-2	Xiaofeng	Sun
F1-3	Xiaoqing	Sun
F1-4	Xiaoqing	Sun

F2-6	Xiaoqing	Sun
F4-5	Xiaoqing	Sun
E2-3	Xinqi	Sun
D1-4	Yilin	Sun
P2-1	Yilin	Sun
P2-6	Yilin	Sun
P2-7	Yilin	Sun
B2-5	Yingxue	Sun
P1-39	Yingxue	Sun
B7-2	Yongsheng	Sun
E6-3	Yongsheng	Sun
E1-2	Yuan	Sun
P1-61	Yuting	Sun
F3-4	Zhong	Sun
D6-3	Zixuan	Sun
E5-1	Zixuan	Sun
K1-2	Myung Hoon	Sunwoo
C6-1	Henrique Iha	Taguti
D3-4	Wei-Heng	Tai
B7-5	Misaki	Takagi
D4-4	Kiyoshi	Takeuchi
E1-3	Jialei	Tan
P1-9	Hailong	Tang
P1-5	Jiami	Tang
C2-3	Jing	Tang
P1-13	Jinsheng	Tang
E2-2	Juan	Tang
P2-12	Peishun	Tang
E3-4	Xiaoyan	Tang
P2-34	Xiaoyan	Tang
P2-40	Xiaoyan	Tang
P2-41	Xiaoyan	Tang
P2-43	Xiaoyan	Tang
P1-22	Yiwen	Tang
B4-2	He	Tang
E2-1	Nick	Tao
P1-44	Qiuyu	Tao
A1-5	Yongjin	Tao
D3-1	Li	Tao
E3-2	Stephen	Taylor
P2-25	Changjiu	Teng
P2-58	Qiao	Teng
C3-1	Bharatha Kumar	Thangarasu

F1-4	Fengbin	Tian
A2-2	Fengshuo	Tian
P1-15	Wei	Tian
P2-13	Xin	Tian
E1-4	Yu	Tian
P1-30	Yuanxin	Tian
F3-1	Ravi	Tiwari
E6-2	Kean Hong	Tok
D2-1	Akira	Tsuchiya
A2-5	Jiangtao	Tu
D2-3	Ruei-Cheng	Tu
SS-2	Do Anh	Tuan
C3-4	Ziyu	Wagn
P2-38	Li	Wan
P2-45	Li	Wan
P2-46	Li	Wan
P1-16	Peng	Wan
P2-39	Qingbo	Wan
P2-18	Wenqing	Wan
E3-3	Yuxi	Wan
E7-3	Yuxi	Wan
P2-47	Yuxi	Wan
P2-57	Yuxi	Wan
K3-2	Albert	Wang
C7-2	Anqing	Wang
C3-1	Bo	Wang
A4-5	Bohan	Wang
D2-5	Bo-Wei	Wang
P1-15	Chao	Wang
A2-1	Chenghua	Wang
P2-33	Chenlu	Wang
P2-29	Chenxing	Wang
C7-2	Chenxu	Wang
B2-1	Chua-Chin	Wang
P1-16	Chunlin	Wang
F4-3	Cuimei	Wang
F4-4	Cuimei	Wang
C5-2	Dejin	Wang
E1-2	Denggui	Wang
P2-15	Fangzhou	Wang
P2-16	Feixiong	Wang
P1-24	Haiwei	Wang
P2-48	Haoran	Wang
D6-3	Hongbo	Wang

P2-6	Huaipeng	Wang
P2-7	Huaipeng	Wang
E2-2	Huaishan	Wang
B1-3	Jiabin	Wang
B7-3	Jian	Wang
B7-4	Jian	Wang
P1-17	Jijiang	Wang
C1-3	Jinxiang	Wang
P2-20	Jinxiang	Wang
P2-42	Jitong	Wang
D1-2	Junfei	Wang
D7-4	Junhao	Wang
P2-11	Kaifeng	Wang
A2-2	Kaixuan	Wang
D5-2	Kang	Wang
B1-4	Langyuan	Wang
P1-65	Lei	wang
P1-54	Li	Wang
A3-5	Liang	Wang
B3-5	Lina	Wang
P1-8	Lina	Wang
C1-5	Lingli	Wang
C2-4	Lingli	Wang
C2-6	Lingli	Wang
E7-3	Long	Wang
B4-5	Longsheng	Wang
P1-47	Longsheng	Wang
C4-1	Luda	Wang
C7-2	Meng	Wang
A5-6	Mengxuan	Wang
A5-4	Mingbo	Wang
A1-3	Mingyu	Wang
P1-23	Mingyu	Wang
P2-61	Mingyu	Wang
E2-1	Peiran	Wang
P2-10	Peng	Wang
P2-17	Peng	Wang
A2-3	Pengjun	Wang
A2-4	Pengjun	Wang
A5-3	Pengjun	Wang
P1-57	Pengjun	Wang
P2-27	Qianwen	Wang
P2-28	Qianwen	Wang
E2-1	Qing	Wang

C4-4	Ruibo	Wang
A1-6	Runsheng	Wang
B7-1	Runsheng	Wang
C2-2	Runsheng	Wang
D6-2	Runsheng	Wang
D6-3	Runsheng	Wang
D7-3	Runsheng	Wang
E5-1	Runsheng	Wang
F6-4	Runsheng	Wang
P2-11	Runsheng	Wang
P1-19	Shidong	Wang
F3-4	Shiqing	Wang
P2-59	Shuhan	Wang
F7-3	Songyao	Wang
B2-3	Teng	Wang
C3-4	Tianrui	Wang
P1-62	Tianrui	Wang
P2-45	Tongyang	Wang
P2-46	Tongyang	Wang
E6-1	W.	Wang
F7-4	Wenbin	Wang
P2-51	Wenbin	Wang
F1-3	Wenwu	Wang
F1-4	Wenwu	Wang
F2-6	Wenwu	Wang
F4-5	Wenwu	Wang
P2-64	Xiang	Wang
P1-1	Xianglong	Wang
P2-24	Xiao	Wang
P2-15	Xiaohui	Wang
F1-3	Xiaolei	Wang
F1-4	Xiaolei	Wang
F2-6	Xiaolei	Wang
F4-5	Xiaolei	Wang
C7-2	Xiaoli	Wang
E3-3	Xiaoming	Wang
P2-47	Xiaoming	Wang
E3-3	Xiaoping	Wang
C6-6	Xiaowei	Wang
P1-6	Xin’an	Wang
F2-1	Xuebin	Wang
E3-3	Yan	Wang
P1-58	Yan	Wang
P2-15	Yang	Wang

P2-54	Yang	Wang
F2-1	Yeliang	Wang
P2-59	Yijiao	Wang
A1-3	Yixue	Wang
P2-59	Yongjia	Wang
E2-2	Yu	Wang
A1-6	Yuan	Wang
A4-3	Yuchen	Wang
B6-1	Yuchen	Wang
C5-3	Yuchen	Wang
P1-48	Yuchen	Wang
F2-7	Yuhan	Wang
P1-68	Zelin	Wang
E3-2	Zhao	Wang
B1-1	Zhaobin	Wang
C1-4	Zhengzhuo	Wang
D7-1	Zhenyu	Wang
D1-2	Zhichong	Wang
P2-63	Zhichong	Wang
K2-1	Zhonglin	Wang
P2-10	Zhonrui	Wang
B2-3	Zijun	Wang
P1-3	Ziqiang	Wang
D6-3	Zirui	Wang
A5-2	Ziyao	Wang
P1-62	Ziyu	Wang
F4-3	Zongwei	Wang
F4-4	Zongwei	Wang
P1-4	Yixing	Wang
B3-2	Hailong	Wei
P1-15	Jiangbo	Wei
B7-5	Jianglin	Wei
B4-1	Jiangling	Wei
E1-4	Jiaxing	Wei
P2-31	Jiaxing	Wei
P2-33	Jiaxing	Wei
E1-3	Jie	Wei
E1-5	Jie	Wei
P1-5	Xueming	Wei
E1-5	Yuxi	Wei
B7-4	Zhichao	Wei
E4-3	Hongyang	Wen
A2-5	Liang	Wen
A3-2	Liang	Wen
P1-30	Liang	Wen
D1-3	Xiaokun	Wen
F3-3	Zeping	Weng
P2-2	Kwok-Ho	Wong
D4-3	Man	Wong
E7-1	Eugene	Worley
P2-58	Bo	Wu
A5-6	Chang	Wu
P2-18	Chang	Wu
P2-8	Chang	Wu
A5-4	Cheng	Wu
D4-5	Chunlei	Wu
F5-1	Chunlei	Wu
F5-3	Chunlei	Wu
E2-5	Haibo	Wu
C6-4	HaiQin	Wu
D7-3	Heng	Wu
F6-4	Honglin	Wu
P2-63	Jiabin	Wu
F1-2	Jixuan	Wu
F2-1	Jixuan	Wu
P2-27	Jixuan	Wu
P2-28	Jixuan	Wu
P1-46	Kejun	Wu
P1-55	Liji	Wu
B5-2	Lin	Wu
P2-36	Mei	Wu
C4-1	Ningran	Wu
F5-2	Wangran	Wu
P1-69	Wenhao	Wu
B7-1	Xing	Wu
P1-10	Xing	Wu
P1-9	Xing	Wu
A2-3	Xudong	Wu
C5-1	Yiwen	Wu
P2-11	Yongqin	Wu
C6-5	Yue	Wu
C2-5	Yuping	Wu
P2-38	Yuzhou	Wu
P2-31	Zilong	Wu
A3-2	Donghao	Xia
D6-2	Shiyu	Xia
D4-5	Yiming	Xia
F5-1	Yiming	Xia

F5-3	Yiming	Xia
F2-5	Yinshui	Xia
E3-3	Yun	Xia
P2-47	Yun	Xia
C6-6	Zhiying	Xia
P2-45	Ziming	Xia
P2-46	Ziming	Xia
F1-3	Jinjuan	Xiang
B5-5	Yuguo	Xiang
A4-5	Shanlin	Xiao
D6-2	Yu	Xiao
P2-29	Zhentao	Xiao
P1-51	Chengzhen	Xie
P2-7	Dan	Xie
C3-4	Huiquan	Xie
P1-62	Huiquan	Xie
B1-2	Jing	Xie
P1-43	Mingzhang	Xie
A3-4	Pei-Zhang	Xie
A2-6	Pujin	Xie
P2-14	Qin	Xie
F4-4	Ruiqing	Xie
A3-3	Linlin	Xing
P2-39	Pengcheng	Xing
P1-12	Bingjun	Xiong
P2-22	Ling	Xiong
P1-43	Shisheng	Xiong
C7-1	Sixing	Xiong
D5-3	Wenjuan	Xiong
P2-50	Baohui	Xu
P2-44	Fan	Xu
P2-17	Gaobo	Xu
P1-27	Guohao	Xu
P1-28	Guohao	Xu
A3-1	Hanxi	Xu
F1-3	Hao	Xu
F1-4	Hao	Xu
F2-6	Hao	Xu
F4-5	Hao	Xu
P1-60	Hao	Xu
P1-65	Hao	Xu
B4-4	Hongtao	Xu
C3-2	Hongtao	Xu
C6-5	Hongtao	Xu
D5-3	Jeffrey	Xu
P2-6	Jianlong	Xu
P1-1	Jianqiang	Xu
D5-3	Jing	Xu
P2-59	Jinghan	Xu
B1-2	Jun	Xu
P2-13	Jun	Xu
P2-60	Jun	Xu
P1-24	Long	Xu
A3-2	Mengfan	Xu
A2-3	Ruize	Xu
P1-18	Sheng	Xu
E2-5	Tao	Xu
P1-32	Wei	Xu
F5-2	Wenting	Xu
B1-2	Wenwei	Xu
D1-3	Xinyue	Xu
D4-5	Yumin	Xu
F5-3	Yumin	Xu
P1-55	Hao	Xue
B6-2	Xiaoyong	Xue
F1-5	Xiaoyong	Xue
B7-5	Shuhei	Yamamoto
P1-16	Anzhi	Yan
P2-24	Bei-Ping	Yan
E5-2	Chu	Yan
B5-4	Chuangao	Yan
P1-18	Feng	Yan
P1-48	Feng	Yan
P2-62	Feng	Yan
D5-1	Jin	Yan
B1-4	Na	Yan
P1-60	Na	Yan
P1-65	Na	Yan
C5-4	Shuang	Yan
P2-49	Silu	Yan
E5-2	Wenchao	Yan
F3-3	Wenchao	Yan
P2-32	Xuke	Yan
P2-36	Bowen	Yang
B3-3	Chaowei	Yang
P1-59	Chengyu	Yang
F4-4	Gaoqi	Yang
P2-28	Guangkuo	Yang

F2-1	Guanhua	Yang
E4-1	Hongqiang	Yang
F6-2	Hongqiang	Yang
P2-29	Hongqiang	Yang
P2-32	Hongqiang	Yang
P2-37	Hongqiang	Yang
P2-44	Hongqiang	Yang
F2-6	Jia	Yang
F4-5	Jia	Yang
C4-3	Jie	Yang
P1-52	Jinda	Yang
P2-36	Ling	Yang
E2-4	Ning	Yang
E7-3	Ning	Yang
P2-23	Runhua	Yang
C7-2	Shanqiang	Yang
P2-28	Shaoqi	Yang
D6-2	Sheng	Yang
C2-6	Sijing	Yang
P1-47	Tianqi	Yang
P2-60	Ting	Yang
P2-51	Wangjun	Yang
B1-5	Wenhao	Yang
A4-2	Wu	Yang
P2-38	Yang	Yang
P2-45	Yang	Yang
P2-46	Yang	Yang
P2-17	Yanyu	Yang
P1-40	Yecheng	Yang
P1-16	Yi	Yang
C3-4	Yintang	Yang
P1-1	Yintang	Yang
P1-62	Yintang	Yang
B1-5	Yizhe	Yang
P1-4	Yizhe	Yang
P1-7	Yizhe	Yang
E4-3	Yurui	Yang
E2-3	Yuxiao	Yang
F1-5	Zhangbin	Yang
P1-44	Zhangbin	Yang
F2-5	Zhaohui	Yang
P2-40	Zhen	Yang
P2-45	Zhiyu	Yang
P2-46	Zhiyu	Yang

A1-2	Zhuoyuan	Yang
B7-4	Zixin	Yang
D7-2	Hua	Yao
E3-2	Rui(Ray)	Yao
E6-4	Ruxue	Yao
P2-55	Ruxue	Yao
B3-2	Siyuan	Yao
P1-63	Chenglin	Ye
B4-5	Dongxian	Ye
B5-3	Fan	Ye
B5-5	Fan	Ye
P1-56	Fan	Ye
P1-44	Jiabao	Ye
A1-2	Jinhong	Ye
F2-6	Tianchun	Ye
F4-5	Tianchun	Ye
C3-1	Kiat Seng	Yeo
E4-1	Bo	Yi
P2-32	Bo	Yi
P2-44	Bo	Yi
P1-27	Shiyan	Yi
P1-28	Shiyan	Yi
D6-4	Huaxiang	Yin
P2-10	Huaxiang	Yin
P2-16	Huaxiang	Yin
P2-17	Huaxiang	Yin
B3-6	Rui	Yin
P1-49	Rui	Yin
P1-51	Rui	Yin
P1-53	Rui	Yin
P1-65	Rui	Yin
C1-5	Wenbo	Yin
C2-4	Wenbo	Yin
P1-42	Yizhe	Yin
P2-52	Yizhe	Yin
P2-9	Yizhe	Yin
F4-2	You	Yin
C3-5	Yun	Yin
P2-50	Kai	Ying
C2-3	Hailong	You
F7-4	Hailong	You
P2-51	Hailong	You
F2-2	Fei	Yu
SS-3	Hao	Yu

C3-1	Hongshi	Yu
E2-1	Hongyu	Yu
P1-12	Junjie	Yu
P2-15	Ping	Yu
E4-2	Qiang	Yu
P2-12	Shaofeng	Yu
E7-1	Weiwei	Yu
F2-2	Xiao	Yu
A4-1	Xinglong	Yu
E5-2	Xinwei	Yu
A4-4	Yueru	Yu
D4-5	Yueyuan	Yu
F5-1	Yueyuan	Yu
F5-3	Yueyuan	Yu
P1-46	Zhen	Yu
A2-6	Zhiyi	Yu
A4-5	Zhiyi	Yu
F2-7	Zhiyi	Yu
E6-3	Zhuoqing	Yu
F5-2	Zuoxu	Yu
E3-4	Hao	Yuan
P2-34	Hao	Yuan
P2-43	Hao	Yuan
B2-2	Hengzhou	Yuan
E2-2	Minghong	Yuan
C5-4	Shuai	Yuan
A2-4	Tengfei	Yuan
P1-57	Tengfei	Yuan
P1-35	Xihui	Yuan
P1-20	Yidong	Yuan
E3-3	Wei	Zeng
E7-3	Wei	Zeng
A1-3	Xiaoyang	Zeng
B6-2	Xiaoyang	Zeng
P1-23	Xiaoyang	Zeng
P1-27	Xiaoyang	Zeng
P1-28	Xiaoyang	Zeng
P2-61	Xiaoyang	Zeng
E2-5	Xing	Zeng
P1-60	Yaxin	Zeng
C7-3	Ziye	Zhai
F1-2	Xuepeng	Zhan
P2-27	Xuepeng	Zhan
P2-28	Xuepeng	Zhan

P1-67	Ao	Zhang
E1-3	Bo	Zhang
E2-2	Bo	Zhang
E2-3	Bo	Zhang
E2-4	Bo	Zhang
E2-5	Bo	Zhang
E5-3	Bo	Zhang
E7-2	Bo	Zhang
E7-3	Bo	Zhang
P1-19	Bo	Zhang
P2-30	Bo	Zhang
P2-35	Bo	Zhang
P2-47	Bo	Zhang
P2-57	Bo	Zhang
B2-6	Boyang	Zhang
P1-20	Chenghao	Zhang
	Chenyang	Zhang
T2-2	Chuan	Zhang
P2-42	Chunwei	Zhang
F5-1	David Wei	Zhang
F5-3	David Wei	Zhang
D4-5	DavidWei	Zhang
A1-4	Duoli	Zhang
F6-4	Fangxing	Zhang
E6-1	H.	Zhang
P2-10	Hang	Zhang
B1-4	Hanlu	Zhang
P1-28	Hao	Zhang
P2-26	Hao	Zhang
A5-1	Haoyu	Zhang
A3-6	He	Zhang
P2-53	Hongbo	Zhang
B5-4	Huihong	Zhang
D7-1	Jeff	Zhang
A5-4	Jiaming	Zhang
E6-2	Jian Fu	Zhang
F3-2	Jianfu	Zhang
P1-38	Jianhua	Zhang
A5-2	Jiayu	Zhang
C2-6	Jide	Zhang
E2-1	Jiming	Zhang
P1-50	Jingjing	Zhang
A1-2	Jingtao	Zhang
P2-30	Jinping	Zhang

P2-35	Jinping	Zhang
A4-2	Jun	Zhang
F1-2	Junyu	Zhang
P2-54	Ke	Zhang
P1-58	Lei	Zhang
P1-38	Li	Zhang
C7-2	Liang	Zhang
D6-2	Lining	Zhang
E5-1	Lining	Zhang
F6-4	Lining	Zhang
C5-2	Long	Zhang
E1-2	Long	Zhang
E4-3	Long	Zhang
P1-50	Long	Zhang
C5-1	Lulu	Zhang
P2-10	Meihe	Zhang
P2-36	Meng	Zhang
P1-64	Mengming	Zhang
P1-18	Qianhao	Zhang
P2-10	Qingzhu	Zhang
C7-2	Ruikai	Zhang
P1-24	Runxi	Zhang
C5-2	Sen	Zhang
E5-3	Sen	Zhang
D4-1	Shengdong	Zhang
A4-1	Shengnan	Zhang
A2-4	Shutong	Zhang
B1-4	Shuyu	Zhang
P1-56	Tianyu	Zhang
B4-2	Wei	Zhang
D7-4	WeiDavid	Zhang
E6-2	Weidong	Zhang
F3-2	Weidong	Zhang
P1-35	Weidong	Zhang
D1-3	Wenfeng	Zhang
T1-2	Wentong	Zhang
P1-55	Xiangmin	Zhang
F6-3	Xing	Zhang
F2-7	Xinrui	Zhang
P2-31	Xinyu	Zhang
F6-4	Xinyue	Zhang
C2-5	Xuelian	Zhang
D1-1	Xuewei	Zhang
B3-1	Xusheng	Zhang
B4-3	Yacong	Zhang
P2-16	Yadong	Zhang
A3-5	Yanlong	Zhang
B1-5	Yibo	Zhang
P1-7	Yibo	Zhang
P2-43	Yibo	Zhang
P1-2	Yihui	Zhang
B1-5	Yimeng	Zhang
P1-4	Yimeng	Zhang
P1-7	Yimeng	Zhang
P1-22	Yue	Zhang
A2-4	Yuejun	Zhang
A2-5	Yuejun	Zhang
A3-2	Yuejun	Zhang
P1-30	Yuejun	Zhang
P1-57	Yuejun	Zhang
P1-8	Yuhan	Zhang
B1-5	Yuming	Zhang
C4-4	Yuming	Zhang
E3-4	Yuming	Zhang
E6-4	Yuming	Zhang
P1-4	Yuming	Zhang
P1-7	Yuming	Zhang
P2-34	Yuming	Zhang
P2-40	Yuming	Zhang
P2-41	Yuming	Zhang
P2-49	Yuming	Zhang
P2-55	Yuming	Zhang
A1-4	Yurun	Zhang
B1-5	Yushen	Zhang
P1-7	Yushen	Zhang
E6-4	Yutao	Zhang
P2-55	Yutao	Zhang
F5-2	Yuzhen	Zhang
D6-4	Zhaohao	Zhang
P1-22	Zhen	Zhang
P1-32	Zhenyin	Zhang
P1-24	Zhenyu	Zhang
B3-2	Zhili	Zhang
E5-3	Zhili	Zhang
P2-29	Zonghao	Zhang
P1-13	Chun	Zhao
P1-20	Dongyan	Zhao
D4-5	Fei	Zhao

F5-1	Fei	Zhao
F5-3	Fei	Zhao
D1-4	Hankun	Zhao
C5-4	Kai	Zhao
E1-5	Kai	Zhao
F6-3	Kai	Zhao
A5-3	Liangxiao	Zhao
C5-1	Luyu	Zhao
E1-2	Mengyao	Zhao
P2-13	Peizhi	Zhao
P1-58	Qi	Zhao
P1-42	Ruiyong	Zhao
P2-52	Ruiyong	Zhao
P2-9	Ruiyong	Zhao
P2-25	Shilong	Zhao
B5-1	Shulin	Zhao
P2-28	Xiaohuan	Zhao
P1-47	Xin	Zhao
P1-3	Xu	Zhao
E5-2	Yi	Zhao
F2-4	Yi	Zhao
F3-3	Yi	Zhao
P1-69	Yi	Zhao
A4-1	Yifan	Zhao
P2-45	Yishang	Zhao
P2-46	Yishang	Zhao
A3-5	Yuanfu	Zhao
C5-4	Yudi	Zhao
B7-5	Yujie	Zhao
E3-4	Ziming	Zhao
C5-2	Guiqiang	Zheng
C5-4	Haoping	Zheng
P2-35	Shiwei	Zheng
F7-3	Xuefeng	Zheng
E1-2	Yifei	Zheng
P1-17	Zhe	Zheng
P1-50	Zhe	Zheng
E3-3	Haizhao	Zhi
P2-47	Haizhao	Zhi
P2-64	Chengyan	Zhong
P1-45	Guoqiang	Zhong
B2-5	Jingxue	Zhong
P1-39	Jingxue	Zhong
D6-4	Kun	Zhong
A1-6	Linfeng	Zhong
C5-2	Qingyin	Zhong
B6-3	Tao	Zhong
B2-6	Tianyuan	Zhong
D3-4	Zheng-Hong	Zhong
E7-3	David	Zhou
P2-47	David	Zhou
P2-57	David	Zhou
B5-5	Dayan	Zhou
C2-6	Hao	Zhou
B6-3	Jianjun	Zhou
E1-2	Jianjun	Zhou
P1-67	Jianjun	Zhou
E2-4	Jinggui	Zhou
C1-3	Jinghan	Zhou
P2-20	Jinghan	Zhou
B7-1	Jingming	Zhou
P1-44	Jiuren	Zhou
C6-5	Mohan	Zhou
B6-2	Peng	Zhou
E2-3	Pengwei	Zhou
C2-1	Pingqiang	Zhou
E2-4	Qi	Zhou
E7-3	Qi	Zhou
P2-57	Qi	Zhou
F2-7	Ruibin	Zhou
P2-62	Wenbin	Zhou
P1-31	Wenqian	Zhou
F4-3	Xiahong	Zhou
A4-4	Xiaofang	Zhou
P1-68	Xiaofang	Zhou
P2-26	Xinlong	Zhou
P1-35	Xue	Zhou
C7-2	Yang	Zhou
B2-5	Yongliang	Zhou
P1-39	Yongliang	Zhou
E3-4	Yu	Zhou
P2-34	Yu	Zhou
P2-40	Yu	Zhou
P2-41	Yu	Zhou
B4-5	Zecheng	Zhou
P1-19	Zekun	Zhou
P1-20	Zekun	Zhou
F7-2	Zheng	Zhou

P1-63	Zheng	Zhou
P2-25	Zhuoling	Zhou
P1-34	Zikang	Zhou
E3-3	Ziwei	Zhou
A5-3	Ziyu	Zhou
P2-63	Chiang	Zhu
E2-4	Jianggen	Zhu
E7-3	Jianggen	Zhu
P2-57	Jianggen	Zhu
F2-4	Saike	Zhu
D6-1	Xiaona	Zhu
P2-12	Xiaona	Zhu
P2-63	Xiaona	Zhu
P1-52	Xing	Zhu
B4-5	Yexin	Zhu
P1-32	Yinna	Zhu
P1-38	Yujie	Zhu
A5-7	Zhangming	Zhu
B4-5	Zhangming	Zhu
B5-2	Zhangming	Zhu
P1-47	Zhangming	Zhu
D7-4	Zhiyuan	Zhu
B3-1	Quanrong	Zhuang
A1-2	Tianshu	Zhuo
P1-31	Qiaosha	Zou
A2-5	Rongxin	Zou
K4-2	Chengjie	Zuo
P2-15	Peng	Zuo

ICSICT 2024 Technical Sessions Overview

Date	Time	Meeting Room 1	Meeting Room 2	Meeting Room 3	Meeting Room 4	Meeting Room 5	Meeting Room 6
Oct.22	9:00:12:15	**Tutorial Session T1** (Meeting Room 8)					
	13:30-18:30	**Tutorial Session T2** (Meeting Room 8)					
Oct.23	8:30-9:00	**Opening** (Grand Ball Room)					
	9:00-10:30	**Keynote Session K1** (Grand Ball Room)					
	10:45-12:15	**Keynote Session K2** (Grand Ball Room)					
	13: 30-15: 15	**Special Session** the Future of AI	**Session B1** Analog Circuit I	**Session C1** EDA I	**Session D1** Novel Device I	**Session E1** Power Device I	**Session F1** Memory Device I
	15: 30-17: 15		**Session B2** Analog Circuit II	**Session C2** EDA II	**Session D2** Novel Device II	**Session E2** Power Device II	**Session F2** Memory Device II
	17: 15-18：30	**Poster Session I** (1st Fl.)					
	19: 00-21: 00	**Reception**					
Oct.24	9:00-10: 30	**Keynote Session K3** (Grand Ball Room)					
	10: 45-12: 15	**Panel Discussion** (Grand Ball Room)					
	13: 30-15: 30	**Session A1** AI Circuit	**Session B3** Analog Circuit III	**Session C3** RF Circuit I	**Session D3** Novel Device III	**Session E3** Power Device III	**Session F3** Memory Device III
	15: 30-17: 15	**Session A2** Security	**Session B4** Mixed Signal I	**Session C4** Sensor & MEMS I	**Session D4** Novel Device IV	**Session E4** Power Device IV	**Session F4** Memory Device IV
	17: 15-18：30	**Poster Session II** (1st Fl.)					
Oct.25	9:00-10: 30	**Keynote Session K4** (Grand Ball Room)					
	10: 45-12: 15	**Session A3** Digital & Memory Circuit	**Session B5** Mixed Signal II	**Session C5** Sensor & MEMS II	**Session D5** Process I	**Session E5** Reliability I	**Session F5** Device Modeling I
	13: 30-15: 30	**Session A4** Processor	**Session B6** Mixed Signal III	**Session C6** RF Circuit II	**Session D6** Process II	**Session E6** Reliability II	**Session F6** Device Modeling II
	15: 30-17: 15	**Session A5** FPGA Based Design	**Session B7** Chip Test	**Session C7** Sensor & MEMS III	**Session D7** 3D Integration	**Session E7** Reliability III	**Session F7** Device Modeling III
	19: 00-21: 00	**Closing & Banquet**					

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